

TZ68K+ UEFI BIOS Manual

UEFI BIOS Setup	1
1 Main Menu	3
2 Advanced Menu.....	4
3 Chipset Menu	17
4 Boot Menu.....	22
5 Security Menu	24
6 O.N.E Menu	25
7 Exit Menu	31

TZ68K+ UEFI BIOS Manual

UEFI BIOS Setup

Introduction

The purpose of this manual is to describe the settings in the AMI UEFI BIOS Setup program on this motherboard. The Setup program allows users to modify the basic system configuration and save these settings to NVRAM.

UEFI BIOS determines what a computer can do without accessing programs from a disk. This system controls most of the input and output devices such as keyboard, mouse, serial ports and disk drives. BIOS activates at the first stage of the booting process, loading and executing the operating system. Some additional features, such as virus and password protection or chipset fine-tuning options are also included in UEFI BIOS.

The rest of this manual will to guide you through the options and settings in UEFI BIOS Setup.

Plug and Play Support

This AMI UEFI BIOS supports the Plug and Play Version 1.0A specification.

EPA Green PC Support

This AMI UEFI BIOS supports Version 1.03 of the EPA Green PC specification.

ACPI Support

AMI ACPI UEFI BIOS support Version 1.0/2.0 of Advanced Configuration and Power interface specification (ACPI). It provides ASL code for power management and device configuration capabilities as defined in the ACPI specification, developed by Microsoft, Intel and Toshiba.

PCI Bus Support

This AMI UEFI BIOS also supports Version 2.3 of the Intel PCI (Peripheral Component Interconnect) local bus specification.

DRAM Support

DDR3 SDRAM (Double Data Rate III Synchronous DRAM) is supported.

TZ68K+ UEFI BIOS Manual

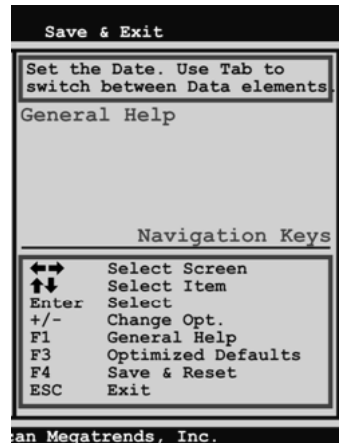
Supported CPUs

This AMI UEFI BIOS supports the Intel CPU.

Using Setup

When starting up the computer, press during the **Power-On Self-Test (POST)** to enter the UEFI BIOS setup utility.

In the UEFI BIOS setup utility, you will see **General Help** description at the top right corner, and this is providing a brief description of the selected item. **Navigation Keys** for that particular menu are at the bottom right corner, and you can use these keys to select item and change the settings.



Notice

- The default UEFI BIOS settings apply for most conditions to ensure optimum performance of the motherboard. If the system becomes unstable after changing any settings, please load the default settings to ensure system's compatibility and stability. Use Load Setup Default under the Exit Menu.
- For better system performance, the UEFI BIOS firmware is being continuously updated. The UEFI BIOS information described in this manual is for your reference only. The actual UEFI BIOS information and settings on board may be slightly different from this manual.
- The content of this manual is subject to be changed without notice. We will not be responsible for any mistakes found in this user's manual and any system damage that may be caused by wrong-settings.

TZ68K+ UEFI BIOS Manual

1 Main Menu

Once you enter AMI UEFI BIOS Setup Utility, the Main Menu will appear on the screen providing an overview of the basic system information.



BIOS Information

Shows system information including UEFI BIOS version, model name, marketing name, build date, etc.

Total Memory

Shows system memory size, VGA shard memory will be excluded.

System Date

Set the system date. Note that the 'Day' automatically changes when you set the date.

System Time

Set the system internal clock.

Access Level

Shows the access level of current user.

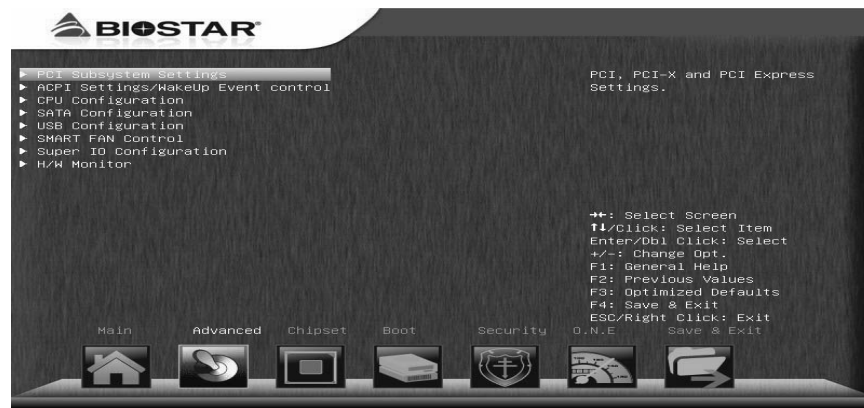
TZ68K+ UEFI BIOS Manual

2 Advanced Menu

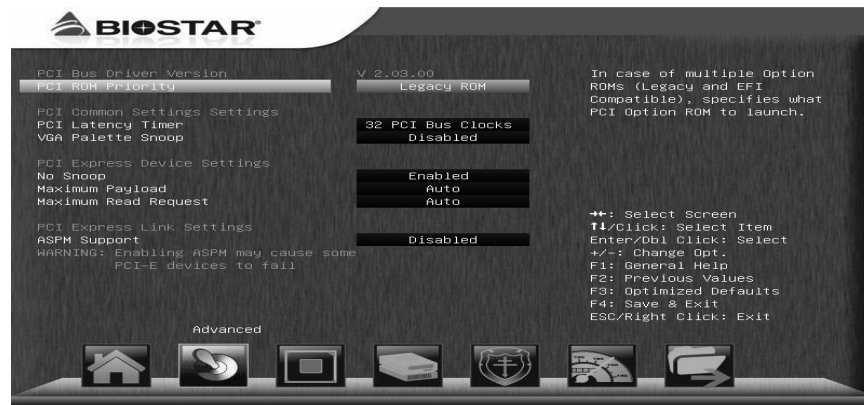
The Advanced Menu allows you to configure the settings of CPU, Super I/O, Power Management, and other system devices.

Notice

- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



PCI Subsystem Settings



TZ68K+ UEFI BIOS Manual

PCI ROM Priority

In case of multiple option ROMs (Legacy and EFI Compatible), this item specifies what PCI Option ROM to launch

Options: Legacy ROM (Default) / EFI Compatible ROM

PCI Latency Timer

This item sets the value to be programmed into PCI Latency Timer Register.

Options: 32 PCI Bus Clocks (Default) / 64 PCI Bus Clocks / 96 PCI Bus Clocks / 128 PCI Bus Clocks / 160 PCI Bus Clocks / 192 PCI Bus Clocks / 224 PCI Bus Clocks / 248 PCI Bus Clocks

VGA Palette Snoop

This item enables or disables VGA Palette Registers Snooping.

Options: Disabled (Default) / Enabled

No Snoop

This item enables or disables PCI Express Device No Snoop option.

Options: Enabled (Default) / Disabled

Maximum Payload

This item sets Maximum Payload of PCI Express Device or allows System BIOS to select the value.

Options: Auto (Default) / 128 Bytes / 256 Bytes / 512 Bytes / 1024 Bytes / 2048 Bytes / 4096 Bytes

Maximum Read Request

This item sets Maximum Read Request Size of PCI Express Device or allows System BIOS to select the value.

Options: Disabled (Default) / Enabled

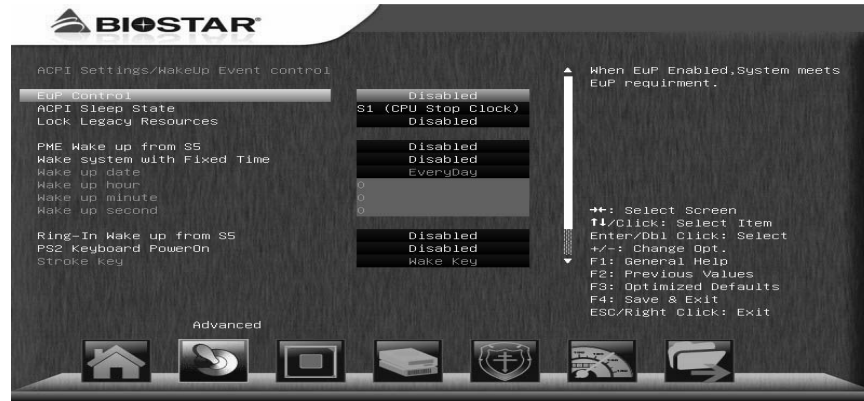
ASPM Support

This item sets the ASPM Level: Force LO – Force all links to LO State; Auto – BIOS auto configures; Disabled – Disables ASPM.

Options: Disabled (Default) / Auto / Force LO

TZ68K+ UEFI BIOS Manual

ACPI Settings/WakeUp Event control



EuP Control

When EuP is enabled, the system will meet EuP requirement.

Options: Disabled (Default) / Enabled

ACPI Sleep State

This item selects the highest ACPI sleep state the system will enter when the SUSPEND button is pressed.

Options: S1 (CPU Stop Clock) (Default) / Suspend Disabled / S3 (Suspend to RAM)

Lock Legacy Resources

The item allows you to lock legacy resources.

Options: Disabled (Default) / Enabled

PME Wake up from S5

The item enables the system to wake from S5 using PME event.

Options: Disabled (Default) / Enabled

TZ68K+ UEFI BIOS Manual

Wake system with Fixed Time

This item enables or disables the system to wake on by alarm event. When this item is enabled, the system will wake on the hr::min::sec specified.

Options: Disabled (Default) / Enabled

Wake up date

You can choose which date the system will boot up.

Wake up hour / Wake up minute / Wake up second

You can choose the system boot up time, input hour, minute and second to specify.

Ring-In Wake up from S5

This item enables the system to wake from S5 using Ring-In event.

Options: Disabled (Default) / Enabled

PS2 Keyboard PowerOn

This item allows you to control the keyboard power on function.

Options: Disabled (Default) / Any Key / Stroke Key / Specific Key

Stroke Keys Selected

This item will show only when Keyboard PowerOn is set "Stroke Key."

Options: Wake Key (Default) / Power Key / Ctrl+F1 / Ctrl+F2 / Ctrl+F3 /
Ctrl+F4 / Ctrl+F5 / Ctrl+F6

Specific Key Enter

This item will show only when Keyboard PowerOn is set "Specific Key." Press Enter to set Specific key.

PS2 Mouse PowerOn

This item allows you to control the mouse power on function.

Options: Disabled (Default) / Enabled

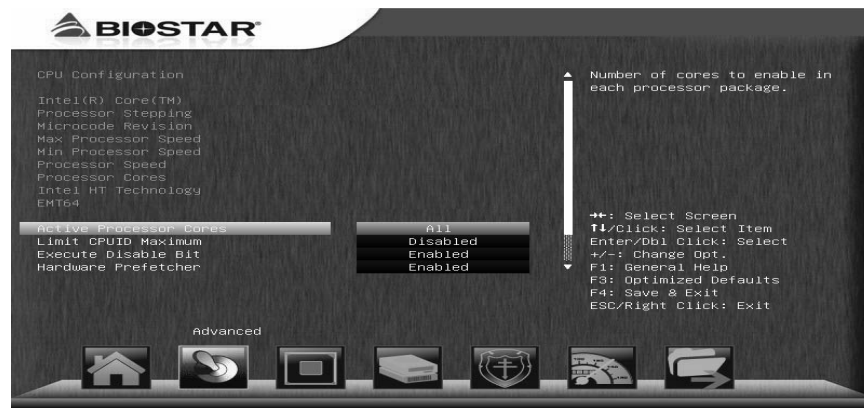
TZ68K+ UEFI BIOS Manual

USB Device Wakeup from S3/S4

This item allows you to enable or disabled the USB resume from S3/S4 function.

Options: Disabled (Default) / Enabled

CPU Configuration



Active Processor Cores

This item sets number of cores to enable in each processor package.

Options: All (Default) / 1 / 2 / 3

Limit CPUID Maximum

When the computer is booted up, the operating system executes the CPUID instruction to identify the processor and its capabilities. Before it can do so, it must first query the processor to find out the highest input value CPUID recognizes. This determines the kind of basic information CPUID can provide the operating system.

Options: Disabled (Default) / Enabled

Execute-Disable Bit

XD can prevent certain classes of malicious buffer overflow attacks when combined with a supporting OS (Windows Server 2003 SP1, Windows XP SP2, SuSE Linux 9.2, RedHat Enterprise 3 Update 3.).

Options: Enabled (Default) / Disabled

TZ68K+ UEFI BIOS Manual

Hardware Prefetcher

The processor has a hardware prefetcher that automatically analyzes its requirements and prefetches data and instructions from the memory into the Level 2 cache that are likely to be required in the near future. This reduces the latency associated with memory reads.

Options: Enabled (Default) / Disabled

Adjacent Cache Line Prefetch

The processor has a hardware adjacent cache line prefetch mechanism that automatically fetches an extra 64-byte cache line whenever the processor requests for a 64-byte cache line. This reduces cache latency by making the next cache line immediately available if the processor requires it as well.

Options: Enabled (Default) / Disabled

Intel Virtualization Tech

Virtualization Technology can virtually separate your system resource into several parts, thus enhance the performance when running virtual machines or multi interface systems.

Options: Disabled (Default) / Enabled

Power Technology

This item enables the power management features.

Options: Energy Efficient (Default) / Disabled / Custom

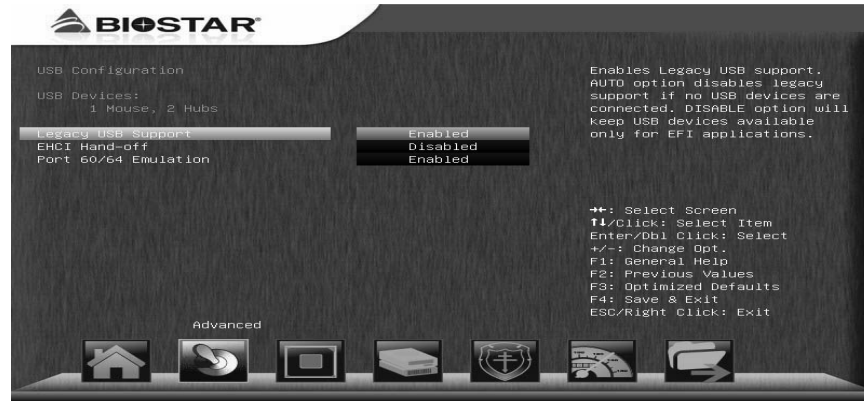
Local x2APIC

This item enables Local x2APIC. Some OSes do not support this feature.

Options: Disabled (Default) / Enabled

TZ68K+ UEFI BIOS Manual

USB Configuration



Legacy USB Support

This item determines if the BIOS should provide legacy support for USB devices like the keyboard, mouse, and USB drive. This is a useful feature when using such USB devices with operating systems that do not natively support USB (e.g. Microsoft DOS or Windows NT).

Options: Enabled (Default) / Disabled / Auto

EHCI Hand-Off

This is a workaround for OSes without EHCI hand-off support. The EHCI ownership change should be claimed by EHCI driver.

Options: Disabled (Default) / Enabled

Port 60/64 Emulation

This item enables I/O port 60h/64h emulation support. This should be enabled for the complete USB keyboard legacy support for non-USB aware OSes.

Options: Enabled (Default) / Disabled

TZ68K+ UEFI BIOS Manual

SMART FAN Control



CPU Smart FAN

This item allows you to control the CPU Smart Fan function.

Options: Disabled (Default) / Auto

CPU FAN Calibrate

Press [ENTER] to calibrate CPU FAN.

Control Mode

This item provides several operation modes of the fan.

Options: Quiet / Aggressive / Manual

Fan Ctrl OFF(°C)

When CPU temperature is lower than this value, the CPU fan will keep lowest RPM.

Options: 10 (°C) (default)

Fan Ctrl On(°C)

When CPU temperature is higher than this value, the CPU fan controller will turn on.

Options: 20 (°C) (Default)

TZ68K+ UEFI BIOS Manual

Fan Ctrl Start Value

This item sets CPU FAN Start Speed Value.

Options: 50 (Default)

Fan Ctrl Sensitive

The bigger the numeral is, the higher the FAN speed is.

Options: 30 (Default)

Super IO Configuration



Restore AC Power Loss

This setting specifies how your system should behave after a power fail or interrupts occurs. Power Off: Leaving the system in power-off status after power recovers. Power ON: Powering on the system immediately when power returns. Last State: 1. Leaving the system in power-off if the system shuts down at DC off status; 2. Powering on the system immediately if the system shuts down at DC on status.

Options: Power Off (Default) / Power On / Last State

TZ68K+ UEFI BIOS Manual

Serial Port 1 Configuration



Serial Port

This item enables or disables Serial Port (COM).

Options: Enabled (Default) / Disabled

Change Settings

This item selects an optimal setting for Super IO device.

Options: Auto (Default) / IO=3F8h; IRQ=4 / IO=3F8h; IRQ=3,4,5,6,7,10,11,12 / IO=2F8h; IRQ=3,4,5,6,7,10,11,12 / IO=3E8h; IRQ=3,4,5,6,7,10,11,12 / IO=2E8h; IRQ=3,4,5,6,7,10,11,12

Parallel Port Configuration



TZ68K+ UEFI BIOS Manual

Parallel Port

This item enables or disables Parallel Port (LPT/LPTE).

Options: Enabled (Default) / Disabled

Change Settings

This item allows you to select an optimal setting for Super IO device.

Options: Auto (Default) / IO=378h; IRQ=5 / IO=378h; IRQ=5, 6, 7, 9, 10, 11, 12 / IO=278h; IRQ=5, 6, 7, 9, 10, 11, 12 / IO=3BCh; IRQ=5, 6, 7, 9, 10, 11, 12

Device Mode

This item allows you to determine how the parallel port should function.

Options: Standard Parallel Port Mode (Default) (Using Parallel port as Standard Printer Port) /
EPP Mode (Using Parallel Port as Enhanced Parallel Port) /
ECP Mode (Using Parallel port as Extended Capabilities Port) /
ECP Mode & EPP Mode (Using Parallel port as ECP & EPP mode)

CIR Controller Configuration



CIR Controller

This item enables or disables CIR Controller.

Options: Disabled (Default) / Enabled

Change Settings

This item selects an optimal setting for Super IO device.

Options: Auto (Default)

TZ68K+ UEFI BIOS Manual

H/W Monitor



Shutdown Temperature

This item allows you to set up the CPU shutdown Temperature.

Options: Disabled (Default) / 70°C/158°F / 75°C/167°F / 80°C/176°F / 85°C/185°F / 90°C/194°F

TZ68K+ UEFI BIOS Manual

3 Chipset Menu

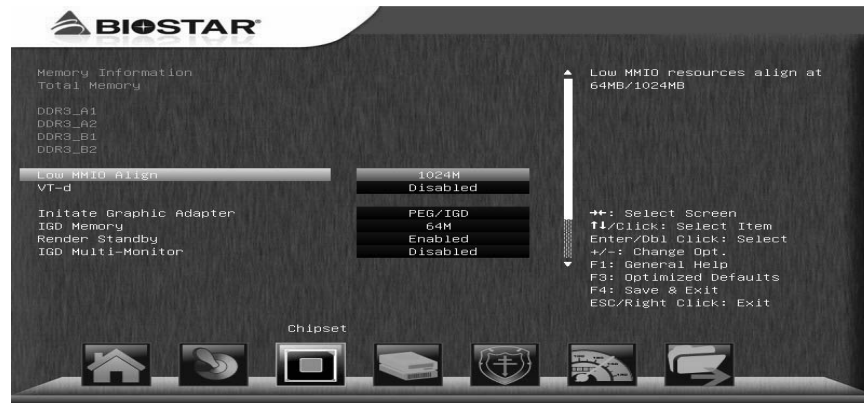
This section describes configuring the PCI bus system. PCI, or Personal Computer Interconnect, is a system which allows I/O devices to operate at speeds nearing the speed of the CPU itself uses when communicating with its own special components.

Notice

- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



North Bridge



TZ68K+ UEFI BIOS Manual

Low MMIO Align

Low MMIO resources align at 64MB/1024MB.

Options: 1024M (Default) / 64M

VT-d

This item enables/disables VT-d function.

Options: Disabled (Default) / Enabled

Initiate Graphic Adapter

This item allows you to select which graphics controller to use as the primary boot device.

Options: PEG/PCI (Default) / IGD / PCI/IGD / PCI/PEG / PEG/IGD

IGD Memory

This item sets IGD Share Memory Size.

Options: 64M (Default) / Disabled / 32M / 128M

Render Standby

This item enables/disables Render Standby by Internal Graphics Device.

Options: Enabled (Default) / Disabled

IGD Multi-Monitor

This item enables/disables IGD Multi-Monitor by Internal Graphics Device.

Options: Disabled (Default) / Enabled

DVMT Mode Select

This item selects DVMT Mode used by Internal Graphics Device.

Options: DVMT Mode (Default) / Fixed Mode

TZ68K+ UEFI BIOS Manual

DVMT/FIXED Memory Size

DVMT stands for “Dynamic Video Memory Technology”. This is an enhancement of the unified memory architecture (UMA) concept. DVMT will set the optimum amount of memory to be allocated for a balance between graphics and system performance. DVMT dynamically respond to system requirements and applications demands, by allocating the proper amount of display, texturing and buffer memory after the operating system has booted.

Options: 256MB (Default) / 128MB / Maximum

PCI Express Port

This item allows user to control PCI Express x16 Port.

Options: Auto (Default) / Disabled / Enabled

PEG Force GEN1

Some non-graphics PCI-E devices may not follow PCI-E Specification and may incorrectly report their Gen capability or link width.

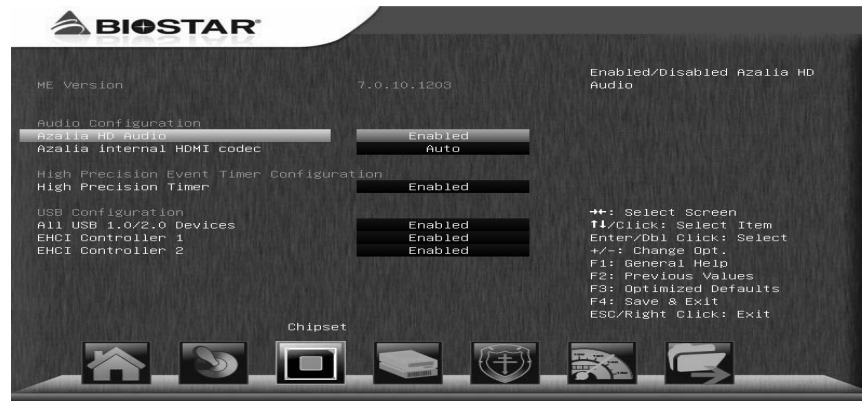
Options: Disabled (Default) / Enabled

Detect Non-Compliance Device

Detect Non-Compliance PCI Express Device in PEG.

Options: Enabled (Default) / Disabled

South Bridge Configuration



TZ68K+ UEFI BIOS Manual

Azalia HD Audio

This item enables/disables Azalia HD Audio.

Options: Enabled (Default) / Disabled

Azalia internal HDMI codec

This item enables/disables internal HDMI codec for Azalia.

Options: Enabled (Default) / Disabled

High Precision Timer

This item enables/disables the High Precision Event Timer.

Options: Enabled (Default) / Disabled

All USB 1.0/2.0 Devices

This item enables/disables All USB 1.0/2.0 Devices.

Options: Enabled (Default) / Disabled

EHCI Controller 1

This item enables/disables USB 2.0 (EHCI) Support.

Options: Enabled (Default) / Disabled

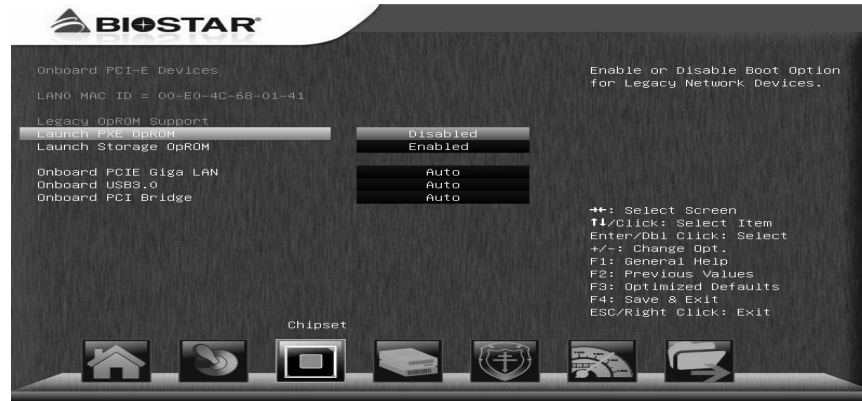
EHCI Controller 2

This item enables/disables USB 2.0 (EHCI) Support.

Options: Enabled (Default) / Disabled

TZ68K+ UEFI BIOS Manual

Onboard PCI-E Devices



Launch PXE OpROM

This item enables/disables Boot Option for Legacy Network Devices.

Options: Disabled (Default) / Enabled

Launch Storage OpROM

This item enables/disables Boot Option for Legacy Mass Storage Devices with Option ROM.

Options: Enabled (Default) / Disabled

Onboard PCIE Giga LAN

This item enables/disables Onboard PCIE Giga LAN.

Options: Auto (Default) / Disabled / Enabled

Onboard USB3.0

This item enables/disables Onboard USB3.0.

Options: Auto (Default) / Disabled / Enabled

Onboard PCI Bridge

This item enables/disables Onboard PCI Bridge.

Options: Auto (Default) / Disabled / Enabled

TZ68K+ UEFI BIOS Manual

4 Boot Menu

This menu allows you to setup the system boot options.



Setup Prompt Timeout

This item sets number of seconds to wait for setup activation key.

Options: 1 (Default)

Bootup NumLock State

This item selects the keyboard NumLock state.

Options: On (Default) / Off

Full Screen LOGO Display

This item allows you to enable/disable Full Screen LOGO Show function.

Options: Enabled (Default) / Disabled

GateA20 Active

Upon Request – FA20 can be disabled using BIOS services. Always – do not allow disabling GA20; this option is useful when any RT code is executed above 1MB

Options: Upon Request (Default) / Always

TZ68K+ UEFI BIOS Manual

Option ROM Messages

This item sets the display mode for Option ROM.
Options: Force BIOS (Default) / Keep Current

Interrupt 19 Capture

Interrupt 19 is the software interrupt that handles the boot disk function. When set to Enabled, this item allows the option ROMs to trap interrupt 19.
Options: Disabled (Default) / Enabled

Boot Success Beep

When this item is set to Enabled, BIOS will let user know boot success with beep.
Options: Enabled (Default) / Disabled

UEFI Boot

This option enables/disables boot from the UEFI Devices.
Options: Disabled (Default) / Enabled

Boot Option #1

The items specify the boot device priority sequence from the available devices. The number of device items that appears on the screen depends on the number of devices installed in the system.

CD/DVD ROM Drive BBS Priorities

This item sets the order of the legacy devices in this group.

Hard Drive BBS Priorities

This item sets the order of the legacy devices in this group.

TZ68K+ UEFI BIOS Manual

5 Security Menu



Administrator Password

This item sets Administrator Password.

User Password

This item sets User Password.

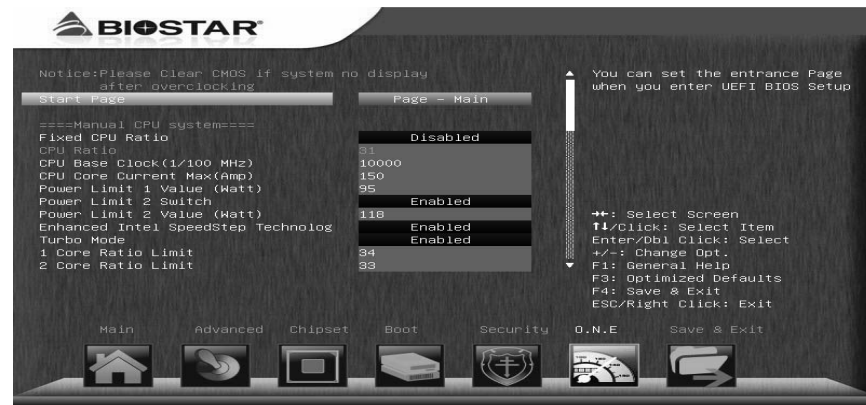
TZ68K+ UEFI BIOS Manual

6 O.N.E Menu

This submenu allows you to change voltage and clock of various devices.
(However, we suggest you use the default setting. Changing the voltage and clock improperly may damage the device.)

Notice

- Beware of that setting inappropriate values in items of this menu may cause system to malfunction.



Start Page

You can set the entrance page when you enter UEFI BIOS Setup.

Options: Page – Main (Default) / Page – Advanced / Page – Chipset / Page – Boot / Page – Security / Page – O.N.E / Page – Save & Exit

Fixed CPU Ratio

This item enables/disables Fixed CPU Ratio all the time.

Options: Disabled (Default) / Enabled

CPU Ratio

This item allows you to set the CPU ratio. This item is adjustable only when Fixed CPU Ratio is set to Enabled.

Options: 31 (Default) / 10 ~ 50

TZ68K+ UEFI BIOS Manual

CPU Base Clock (1/100 MHz)

This item sets CPU Base Clock.

Options: 10000 (Default)

CPU Core Current Max (Amp)

This item sets the Max instantaneous current allowed at any given time.

Options: 150 (Default)

Power Limit 1 Value (Watt)

This item sets the power limit value which CPU must not exceed over a specific time.

Options: 95 (Default)

Power Limit 2 Switch

This item enables/disables Power Limit 2 Switch.

Options: Enabled (Default) / Disabled

Power Limit 2 Value

This item sets Power Limit Value which CPU must not exceed in a short duration time.

Options: 118 (Default)

Enhanced Intel SpeedStep Technology

This item enables/disables Enhanced Intel SpeedStep Technology.

Options: Enabled (Default) / Disabled

Turbo Mode

This item enables/disables Turbo Mode.

Options: Enabled (Default) / Disabled

1/2/3/4 Core Ratio Limit

This item allows user to set core ratio limit.

TZ68K+ UEFI BIOS Manual

CPU C1E

C1E is “Enhanced Halt State” function, this function helps to save power and decrease heat by lowering CPU frequency while the processor is not working.

Options: Enabled (Default) / Disabled

Graphics Core Ratio Limit

This item sets Graphics Core Ratio Limit.

Options: 25 (Default)

DDR3 DRAM Multiplier

This item allows you to set DDR3 DRAM Multiplier.

Options: Auto (Default) / 10.67 / 13.33 / 16 / 18.67 / 21.33

DRAM Timing Control

This item allows you to choose to manually or automatically regulate the DRAM Timing.

Options: By SPD (Default) / Manual / XMP Profile 1 / XMP Profile 2

DRAM Command Rate

This item allows you to select command rate of DDR3.

Options: Auto (Default) / 1T / 2T

CAS# Latency (tCL)

This item allows you to select CAS Latency of DDR3.

Options: 9 (Default) / 3 ~ 15

Row Precharge Time (tRP)

This item allows you to select Row Precharge Time of DDR3.

Options: 9 (Default) / 3 ~ 15

RAS# to CAS# Delay (tRCD)

This item allows you to select Row Address to Column Address Delay of DDR3.

Options: 9 (Default) / 3 ~ 15

TZ68K+ UEFI BIOS Manual

RAS# Active Time (tRAS)

This item allows you to select Row Active Time of DDR3.

Options: 24 (Default) / 9 ~ 63

Write Recovery Time (tWR)

This item allows you to select Internal Write to Read Command Delay of DDR3.

Options: 10 (Default) / 3 ~ 31

Row Refresh Cycle Time (tRFC)

This item allows you to select Minimum Refresh Recovery Time of DDR3.

Options: 74 (Default) / 15 ~ 255

Write to Read Delay (tWTR)

This item allows you to select Internal Write to Read Command Delay of DDR3.

Options: 5 (Default) / 3 ~ 31

Active to Active Delay (tRRD)

This item allows you to select Row Active to Row Active Delay of DDR3.

Options: 4 (Default) / 4 ~ 15

Read CAS# Precharge (tRTP)

This item allows you to select Read to Precharge Delay of DDR3.

Options: 5 (Default) / 4 ~ 15

Four Active Window Delay (tFAW)

This item allows you to select Four Active Window Delay of DDR3.

Options: 20 (Default) / 4 ~ 63

CPU VCore Mode

This item sets the mode of CPU over voltage.

Options: SPEC Voltage (Default) / Auto / Offset Mode / Fixed Mode

CPU VCore Offset

This item sets CPU Vcore Offset Voltage.

TZ68K+ UEFI BIOS Manual

CPU VCore Fixed

This item sets CPU Vcore Fixed Voltage.

IGD VCore Mode

This item sets the mode of the IGD core power supply.

Options: Auto (Default) / SPEC Voltage / Offset Mode / Fixed Mode

IGD VCore Offset

This item sets IGD Vcore Offset Voltage.

IGD VCore Fixed

This item sets IGD Vcore Fixed Voltage.

Phase Control

This item sets PWM Phase Control method.

Options: CPU Power State (Default) / Full On

PWM Work Frequency

This item sets PWM Work Frequency.

Options: Auto (Default) / 200 KHz / 185 KHz / 165 KHz / 145 KHz

PWM I-OUT Override

This item enables/disables PWM I-OUT Override.

Options: Disabled (Default) / Enabled

PWM Thermal Override

This item enables/disables PWM Thermal Override.

Options: Disabled (Default) / Enabled

CPU VCore LoadLine

This item sets VCore LoadLine.

Options: Enabled (Default) / Disabled

DRAM Voltage

This item sets DRAM Voltage.

TZ68K+ UEFI BIOS Manual

Vcc SA

This item sets CPU System Agent (memory controller, DMI, PCIe controllers, and display engine) power supply.

Vcc IO

This item sets CPU High frequency I/O logic power supply.

CPU PLL

This item sets CPU PLL power supply.

PCH Voltage

This item sets PCH Voltage.

DDR3 Slot 1/2/3/4 Information

These items display SPD information of DDR3 memory.



TZ68K+ UEFI BIOS Manual

7 Exit Menu

This menu allows you to load the optimal default settings, and save or discard the changes to the BIOS items.



Discard Changes and Exit

Abandon all changes made during the current session and exit setup.

Save Changes and Reset

Reset the system after saving the changes.

Restore Defaults

This selection allows you to reload the BIOS when problem occurs during system booting sequence. These configurations are factory settings optimized for this system.

Built-in EFI Shell

Press enter to execute UEFI BIOS built-in EFI Shell.

TZ68K+ UEFI BIOS Manual

Saving SetupData to Profile

Saving SetupData to Profile

Restoring SetupData from Profile

Restoring SetupData from Profile