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CHAPTER 1: INTRODUCTION

1.1 FEATURES

A. Hardware

CPU

- Supports Socket 775.
- Supports Intel Pentium 4 processor up to 3.8GHz. (v8.0 supports up to 3.4GHz)
- Supports Dual Code CPU.
- Front side bus at the following frequency ranges:
 - 533MT/s (133MHz Core Clock)
 - 800MT/s (200MHze Core Clock)
- Supports Hyper-Threading Technology.

Chipset

- North Bridge: VIA P4M800CE (for P4M80-M7A v1.0 and v7.0).
- South Bridge: VIA VT8237R PLUS.

Dimension

■ ATX Form Factor: 24.38cm (L) x 20.19cm (W)

Main Memory

- Supports up to 2 DDR devices.
- Supports 133/166/200MHz DDR devices.
- Maximum memory size is up to 2GB. (Following table is only for reference.)

DIMM Socket Location	DDR Module	Total Memory Size
DIMM1	128MB/256MB/512MB/1GB *1	Max is 2GB.
DIMM2	128MB/256MB/512MB/1GB *1	IVIAX IS ZGD.

Super I/O

- Chip: ITE IT8705AF
- Provides the most commonly used legacy super I/O functionality.
- Environment Control initiatives:
 - H/W Monitor,
 - Fan Speed Controller,
 - ITE "Smart Guardian" function.

Onboard IDE

- Support 4 IDE disk drives.
- Supports PIO mode 5, Bus Master, and Ultra DMA 33/66/100/133 function.

LAN

- Chip: RealTek RTL8100C (for P4M80-M7/P4M80-M7A v1.0) VIA VT6103L (for P4M80-M7A v 7.0)
- Supports 10/100 Mb/s auto-negotiation operation.
- Half/Full duplex capability.
- Supports ACPI, PCI power management.

Slot

- 3 PCI bus master slots.
- 1 AGP 4x/8x compatible slot.
- 1 CNR slot.

Serial ATA

- Integrated in VT8237R.
- Supports RAID 0 and RAID 1 functions.
- Supports 2 serial ATA (SATA) ports.
 - Complaints with SATA Version 1.0 specification.
 - Data transfer rates up to 150 MB/s.

Onboard AC'97 Sound Codec

- Chip: ALC655
- Supports 6 channels.
- Supports S/PDIF out function.
- Compliant with AC'97 Version 2.3 specification.

Front Side Onboard Peripherals

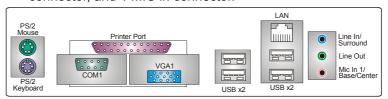
- 1 front panel header supports front panel facilities.
- 1 S/PDIF out connector supports digital audio out function (optional).
- 1 CD-in connector supports 1 CD-ROM audio-in device.
- 1 front audio header supports front panel audio function.
- 1 chassis open header supports PC case-opened warning function.
- 1 Floppy port supports 2 FDD with 360K, 720K, 1.2M, 1.44M and 2.88Mbytes.
- 2 USB headers support 4 USB 2.0 ports.

P4M80-M7 & P4M80-M7A

- 2 IDE connectors support 4 hard disk devices.
- 2 serial ATA connectors support 2 SATA devices.

Rear Side Connectors

- 4 USB 2.0 ports.
- 1 VGA port.
- 1 serial port.
- 1 parallel port.
- 1 RJ-45 LAN jack.
- 1 PS/2 Mouse & Keyboard port.
- 1 vertical audio port including 1 line-in connector, 1 line out connector, and 1 MIC in connector.



B. BIOS & Software

BIOS

- Award legal BIOS.
- Supports APM1.2, ACPI, and USB functions.

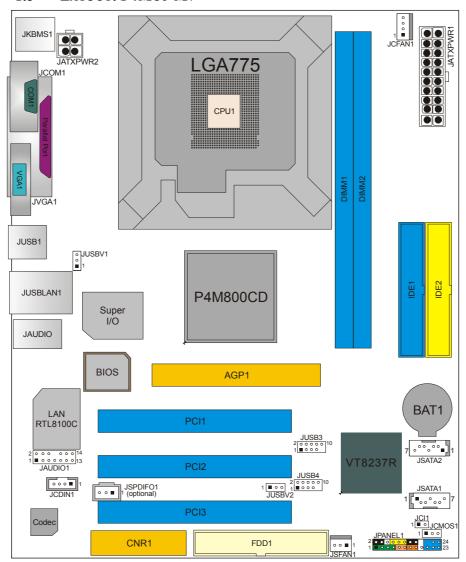
Software

- Supports 9th TouchTM, FlasherTM, WinFlasherTM, and WarpspeederTM.
- Offers the highest performance for Windows 98SE, Windows NT, Windows 2000, Windows ME, Windows XP, Red-Hat Linux, and UNIX series.

1.2 PACKAGE LIST

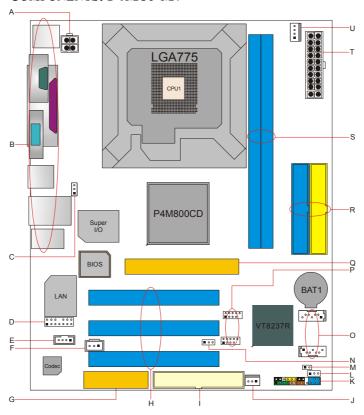
- ♣ FDD cable x1
- HDD cable x1
- User's Manual x1
- ♣ Fully Setup Driver CD x1
- Rear I/O panel for ATX case x1
- ♣ Serial ATA cable x2 (optional)
- S/PDIF out cable x1 (optional)

1.3 LAYOUT: P4M80-M7



Note: represents the 1st pin.

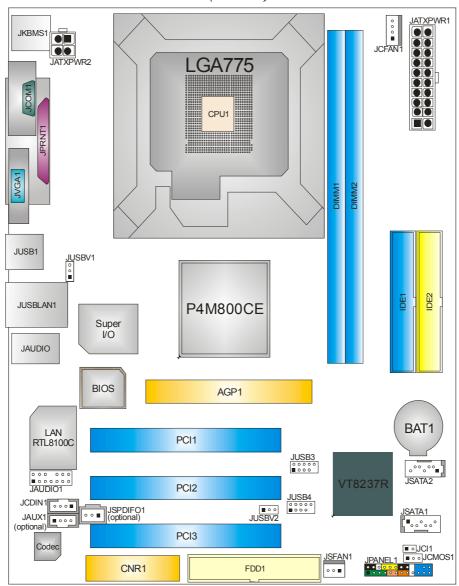
1.4 **COMPONENTS: P4M80-M7**



- **A.** JATXPWR2: ATX power connector.
- B. Back panel connectors (rear side).
- C. JUSBV1: USB power header for JKBMS1, JUSB1, and JUSBLAN1.
- **D.** JAUDIO1: Front panel audio out header.
- E. JCDIN1: CD-ROM audio in connector. P.
- **F.** JSPDIFO1 (optional): Digital audio out **Q.** connector.
- **G.** CNR1: Communication Network Riser **R.** slot.
- **H.** PCI1~3: Peripheral Component Interconnect slots.
- I. FDD1: Floppy disk connector.
- J. JSFAN1: System fan header.
- K. JPANEL1: front panel facilities header.

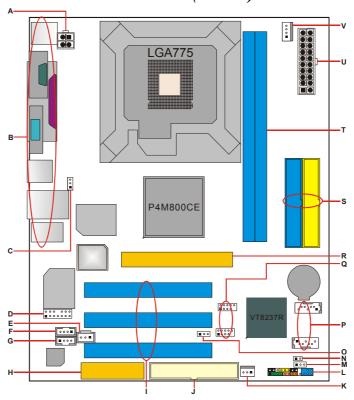
- L. JCMOS1: Clear CMOS header.
- M. JCI1: Chassis open message header.
- **N.** JUSBV2: USB power header for JUSB3/4.
- **O.** JSATA1/2: Serial ATA connectors.
- JUSB3/4: Front USB headers.
- AGP1: Accelerated Graphics Port slot.
- . IDE1/2: Hard disk connectors.
- **S.** DIMM1/2: DDR memory modules.
- **T.** JATXPWR1: ATX power connector.
- U. JCFAN1: CPU fan header.

1.5 LAYOUT: P4M80-M7A (REV 1.0)



Note: ■ represents the 1st pin.

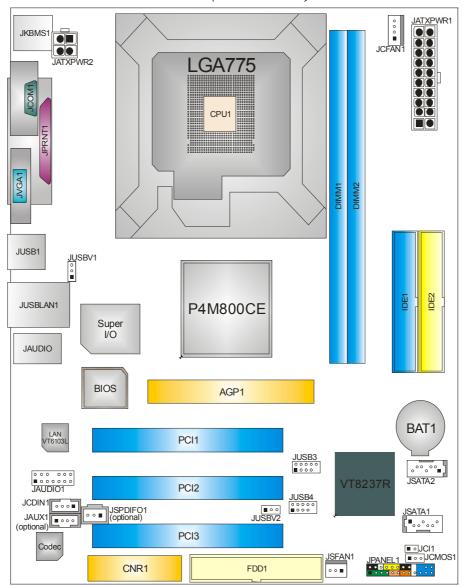
1.6 COMPONENTS: P4M80-M7A (REV 1.0)



- **A.** JATXPWR2: ATX power connector.
- **B.** Back panel connectors (rear side).
- **C.** JUSBV1: USB power header for back panel.
- **D.** JAUDIO1: Front panel audio out header.
- E. JSPDIFO1 (optional): Digital audio out P. connector.
- F. JCDIN1: CD-ROM audio in connector. Q.
- G. CNR1: Communication Network Riser R. slot.
- H. JAUX1:
- I. PCI1~3: Peripheral Component Interconnect slots.
- **J.** FDD1: Floppy disk connector.
- **K.** JSFAN1: System fan header.

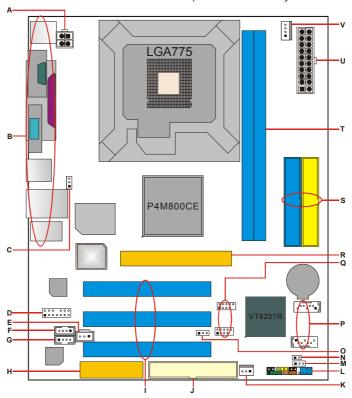
- L. JPANEL1: front panel facilities header.
- M. JCMOS1: Clear CMOS header.
- N. JCI1: Chassis open message header.
- **O.** JUSBV2: USB power header for front panel.
- P. JSATA1/2: Serial ATA connectors.
- Q. JUSB3/4: Front USB headers.
 - AGP1: Accelerated Graphics Port slot.
- S. IDE1/2: Hard disk connectors.
- T. DIMM1/2: DDR memory modules.
- **U.** JATXPWR1: ATX power connector.
- V. JCFAN1: CPU fan header.

1.7 LAYOUT: P4M80-M7A (REV 7.0 & 8.0)



Note: ■ represents the 1st pin.

1.8 COMPONENTS: P4M80-M7A (REV 7.0 & 8.0)



- **A.** JATXPWR2: ATX power connector.
- B. Back panel connectors (rear side).
- **C.** JUSBV1: USB power header for back panel.
- **D.** JAUDIO1: Front panel audio out header.
- E. JSPDIFO1 (optional): Digital audio out P. connector.
- F. JCDIN1: CD-ROM audio in connector. Q.
- G. CNR1: Communication Network Riser R. slot.
- H. JAUX1:
- I. PCI1~3: Peripheral Component Interconnect slots.
- **J.** FDD1: Floppy disk connector.
- **K.** JSFAN1: System fan header.

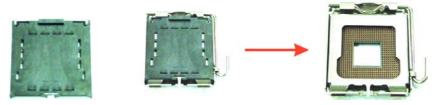
- L. JPANEL1: front panel facilities header.
- M. JCMOS1: Clear CMOS header.
- N. JCI1: Chassis open message header.
- **O.** JUSBV2: USB power header for front panel.
- P. JSATA1/2: Serial ATA connectors.
- Q. JUSB3/4: Front USB headers.
 - AGP1: Accelerated Graphics Port slot.
- S. IDE1/2: Hard disk connectors.
- **T.** DIMM1/2: DDR memory modules.
- **U.** JATXPWR1: ATX power connector.
- V. JCFAN1: CPU fan header.

CHAPTER 2: HARDWARE INSTALLATION

2.1 CENTRAL PROCESSING UNIT (CPU)

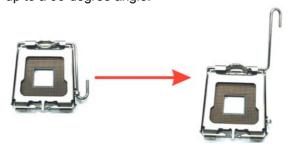
Special Notice:

Remove Pin Cap before installation, and make good preservation for future use. When the CPU is removed, cover the Pin Cap on the empty socket to ensure pin legs won't be damaged.



Pin Cap

Step 1: Pull the lever sideways away from the socket and then raise the lever up to a 90-degree angle.

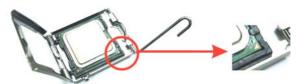


Step 2: Look for the black cut edge on socket, and the white dot on CPU should point wards this black cut edge. The CPU will fit only in the correct orientation.

Step 2-1:



Step 2-2:



Step 3: Hold the CPU down firmly, and then close the lever to complete the installation.



Step 4: Put the CPU Fan on the CPU and buckle it. Connect the CPU FAN power cable to the JCFAN1. This completes the installation.

2.2 FAN HEADERS

These fan headers support cooling-fans built in the computer. The fan wiring and plug may be different according to the fan manufacturer. Connect the fan cable to the connector while matching the black wire to pin#1.

CPU FAN Header: JCFAN1

	¬	Pin	Assignment
0		1	Ground
0		2	Power (+12V)
1	JCFAN1	3	FAN RPM rate sense
		4	Smart Fan Control

System Fan Header: JSFAN1

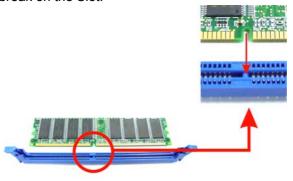
		Pin	Assignment
○ ○ ■ 1		1	Ground
	JSFAN1	2	Power (+12V)
		3	FAN RPM rate sense

Note:

The JCFAN1 and JSFAN1 support 4-pin and 3-pin head connector. When connecting with wires onto connectors, please note that the red wire is the positive and should be connected to pin#2, and the black wire is Ground and should be connected to GND.

2.3 MEMORY MODULE INSTALLATION

 Unlock a DIMM slot by pressing the retaining clips outward. Align a DIMM on the slot such that the notch on the DIMM matches the break on the Slot.



2. Insert the DIMM vertically and firmly into the slot until the retaining chip snap back in place and the DIMM is properly seated.



Note:

To assure the system safety, if you need to change DDR modules, firstly, please unplug the 20-pin power cable with the power connector, and then you can change the modules. Afterwards, plug in the cable the power connector again, and finally you can boot up the system.

2.4 CONNECTORS AND SLOTS

FDD1: Floppy Disk Connector

The motherboard provides a standard floppy disk connector that supports 360K, 720K, 1.2M, 1.44M and 2.88M floppy disk types. This connector supports the provided floppy drive ribbon cables.

IDE1/IDE2: Hard Disk Connectors

The motherboard has a 32-bit Enhanced PCI IDE Controller that provides PIO Mode 0~5, Bus Master, and Ultra DMA 33/ 66/ 100 functionality. It has two HDD connectors IDE1 (primary) and IDE2 (secondary).

The IDE connectors can connect a master and a slave drive, so you can connect up to four hard disk drives. The first hard drive should always be connected to IDE1.

PCI1~PCI3: Peripheral Component Interconnect Slots

This motherboard is equipped with 3 standard PCI slots. PCI stands for Peripheral Component Interconnect, and it is a bus standard for expansion cards. This PCI slot is designated as 32 bits.

AGP1: Accelerated Graphics Port Slot

Your monitor will attach directly to that video card. This motherboard supports video cards for PCI slots, but it is also equipped with an Accelerated Graphics Port (AGP). An AGP card will take advantage of AGP technology for improved video efficiency and performance, especially with 3D graphics.

CNR1: Communication Network Riser Slot

The CNR specification is an open Industry Standard Architecture, and it defines a hardware scalable riser card interface, which supports modem only.

JSATA1/JSATA2: Serial ATA Connectors

The motherboard has a PCI to SATA Controller with 2 channels SATA interface, it satisfies the SATA 1.0 spec and with transfer rate of 1.5Gb/s.

	Pin	Assignment	Pin	Assignment
17	1	Ground	2	TX+
1	3	TX-	4	Ground
JSATA1~2	5	RX-	6	RX+
	7	Ground		_

CHAPTER 3: HEADERS & JUMPERS SETUP

3.1 How to Setup Jumpers

The illustration shows how to set up jumpers. When the jumper cap is placed on pins, the jumper is "close", if not, that means the jumper is "open".







Pin closed



Pin1-2 closed

3.2 **DETAIL SETTINGS**

JUSBV1/JUSBV2: Power Source Headers for USB ports

JUS	BV1/JUSBV2	Assignment	Description
1	Pin 1-2 close	+5V	JUSBV1: +5V for JKBMS1, JUSB1 and JUSBLAN1. JUSBV2: +5V for JUSB3/4.
1	+5V Standby		JUSBV1: JKBMS1, JUSB1 and JUSBLAN1 are powered with +5V standby voltage. JUSBV2: JUSB3/4 are powered by +5V standby voltage.

Note:

In order to support this function "Power-on system via keyboard and mouse", "JUSBV1/JUSBV2" jumper cap should be placed on Pin 2-3.

JUSB3/JUSB4: Front USB Headers

This motherboard provides 2 USB 2.0 headers, which allows user to connect additional USB cable on the PC front panel, and also can be connected with internal USB devices, like USB card reader.

	Pin	Assignment	Pin	Assignment
2 0 0 0 0 0 10	1	+5V (fused)	2	+5V (fused)
1 • • • •	3	USB-	4	USB-
	5	USB+	6	USB+
JUSB3/JUSB4	7	Ground	8	Ground
	9	Key	10	N/A

JATXPWR1/PATXPWR2: Power Connectors

JATXPWR1: This connector allows user to connect with 20-pin power connector on the ATX power supply.

JATXPWR2: By connecting this connector, it will provide +12V to CPU power circuit.

	Pin	Assignment	Pin	Assignment
10 00 20	1	+3.3V	11	+3.3V
2		+3.3V	12	-12V
	3	Ground	13	Ground
	4	+5V	14	PS_ON
	5	Ground	15	Ground
	6	+5V	16	Ground
1 🗖 11	7	Ground	17	Ground
	8	PW_ON	18	-5V
JATXPWR1	9	Standby Voltage +5V	19	+5V
	10	+12V	20	+5V
2001	Pin	Assignment	Pin	Assignment
4 0 0 3	1	+12V	3	Ground
JATXPWR2	2	+12v	4	Ground

JAUDIO1: Front Panel Audio Header

This header allows user to connect the front audio out put cable with the PC front panel. It will disable the output on back panel audio connectors.

	2						
Pin	Assignment	Pin	Assignment				
1	Mic in/center	2	Ground				
3	Mic power/Bass	4	Audio power				
5	Right line out/Speaker out Right	6	Right line out/Speaker out Right				
7	Reserved	8	Key				
9	Left line out/Speaker out Left	10	Left line out/Speaker out Left				
11	Right line in/Rear speaker Right	12	Right line in/Rear speaker Right				
13	Left line in/Rear speaker Left	14	Left line in/Rear speaker Left				

JCDIN1: CD-ROM Audio-in Connector

This connector allows user to connect the audio source from the veriaty devices, like CD-ROM, DVD-ROM, PCI sound card, PCI TV turner card etc..

	Pin	Assignment
	1	Left channel input
JCDIN1	2	Ground
JCDIN1	3	Ground
	4	Right channel input

JSPDIFO1: Digital Audio-out Connector (optional)

This connector allows user to connect the PCI bracket SPDIF output header.

	Pin	Assignment
	1	+5V
JSPDIFO1	2	SPDIF_OUT
00. 2 0.	3	Ground

JCMOS1: Clear CMOS Header

By placing the jumper on pin2-3, it allows user to restore the BIOS safe setting and the CMOS data, please carefully follow the procedures to avoid damaging the motherboard.

JCMOS1	Assignment	
1 Pin 1-2 close	Normal Operation (Default).	
1 ■ ○ ○ Pin 2-3 close	Clear CMOS data.	

% Clear CMOS Procedures:

- 1. Remove AC power line.
- 2. Set the jumper to "Pin 2-3 close".
- 3. Wait for five seconds.
- 4. Set the jumper to "Pin 1-2 close".
- 5. Power on the AC.
- 6. Reset your desired password or clear the CMOS data.

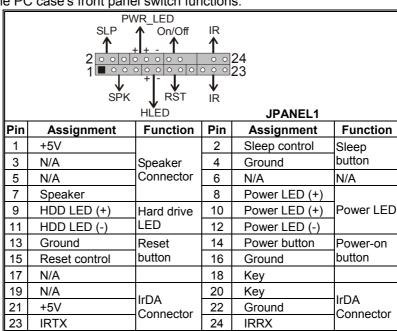
JCI1: Chassis Open Header

This connector allows system to monitor PC case open status. If the signal has been triggered, it will record to the CMOS and show the message on next boot-up.

		Pin	Assignment
1 • •	JCI1	1	Case open signal
	0011	2	Ground

JPANEL1: Front Panel Header

This 24-pin connector includes Power-on, Reset, HDD LED, Power LED, Sleep button, speaker and IrDA Connection. It allows user to connect the PC case's front panel switch functions.



CHAPTER 4: USEFUL HELP

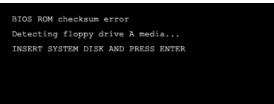
4.1 AWARD BIOS BEEP CODE

Beep Sound	Meaning
One long beep followed by two short beeps	Video card not found or video card memory bad
High-low siren sound	CPU overheated System will shut down automatically
One Short beep when system boot-up	No error found during POST
Long beeps every other second	No DRAM detected or install

4.2 EXTRA INFORMATION

A. BIOS Update

After you fail to update BIOS or BIOS is invaded by virus, the Boot-Block function will help to restore BIOS. If the following message is shown after boot-up the system, it means the BIOS contents are corrupted.



In this Case, please follow the procedure below to restore the BIOS:

- 1. Make a bootable floppy disk.
- 2. Download the Flash Utility "AWDFLASH.exe" from the Biostar website: www.biostar.com.tw
- 3. Confirm motherboard model and download the respectively BIOS from Biostar website.
- 4. Copy "AWDFLASH.exe" and respectively BIOS into floppy disk.
- 5. Insert the bootable disk into floppy drive and press Enter.
- 6. System will boot-up to DOS prompt.
- 7. Type "Awdflash xxxx.bf/sn/py/r" in DOS prompt. (xxxx means BIOS name.)
- 8. System will update BIOS automatically and restart.
- 9. The BIOS has been recovered and will work properly.

B. CPU Overheated

If the system shutdown automatically after power on system for seconds, that means the CPU protection function has been activated.

When the CPU is over heated, the motherboard will shutdown automatically to avoid a damage of the CPU, and the system may not power on again.

In this case, please double check:

- 1. The CPU cooler surface is placed evenly with the CPU surface.
- 2. CPU fan is rotated normally.
- 3. CPU fan speed is fulfilling with the CPU speed.

After confirmed, please follow steps below to relief the CPU protection function.

- 1. Remove the power cord from power supply for seconds.
- 2. Wait for seconds.
- 3. Plug in the power cord and boot up the system.

Or you can:

- Clear the CMOS data.
 (See "Close CMOS Header: JCMOS1" section)
- 2. Wait for seconds.
- 3. Power on the system again.

P4M80-M7 & P4M80-M7A

4.3 TROUBLESHOOTING

TROUBLESHOUTING					
Probable	Solution				
No power to the system at all Power light don't illuminate, fan inside power supply does not turn on. Indicator light on keyboard does	 Make sure power cable is securely plugged in. Replace cable. Contact technical support. 				
not turn on.	Hoing even procesure on both ands of				
System inoperative. Keyboard lights are on, power indicator lights are lit, and hard drive is spinning.	Using even pressure on both ends of the DIMM, press down firmly until the module snaps into place.				
System does not boot from hard disk drive, can be booted from optical drive.	Check cable running from disk to disk controller board. Make sure both ends are securely plugged in; check the drive type in the standard CMOS setup.				
	Backing up the hard drive is extremely important. All hard disks are capable of breaking down at any time.				
System only boots from optical drive. Hard disk can be read and applications	Back up data and applications files.				
can be used but booting from hard disk is impossible.	Reformat the hard drive. Re-install applications and data using backup disks.				
Screen message says "Invalid Configuration" or "CMOS Failure."	Review system's equipment. Make sure correct information is in setup.				
Cannot boot system after installing second hard drive.	Set master/slave jumpers correctly. Run SETUP program and select correct drive types. Call the drive manufacturers for compatibility with other drives.				

CHAPTER 5: WARPSPEEDER™



5.1 Introduction

[WarpSpeeder™], a new powerful control utility, features three user-friendly functions including Overclock Manager, Overvoltage Manager, and Hardware Monitor.

With the Overclock Manager, users can easily adjust the frequency they prefer or they can get the best CPU performance with just one click. The Overvoltage Manager, on the other hand, helps to power up CPU core voltage and Memory voltage. The cool Hardware Monitor smartly indicates the temperatures, voltage and CPU fan speed as well as the chipset information. Also, in the About panel, you can get detail descriptions about BIOS model and chipsets. In addition, the frequency status of CPU, memory, AGP and PCI along with the CPU speed are synchronically shown on our main panel.

Moreover, to protect users' computer systems if the setting is not appropriate when testing and results in system fail or hang, [WarpSpeeder TM] technology assures the system stability by automatically rebooting the computer and then restart to a speed that is either the original system speed or a suitable one.

5.2 System Requirement

OS Support: Windows 98 SE, Windows Me, Windows 2000, Windows XP DirectX: DirectX 8.1 or above. (The Windows XP operating system includes DirectX 8.1. If you use Windows XP, you do not need to install DirectX 8.1.)

5.3 Installation

 Execute the setup execution file, and then the following dialog will pop up. Please click "Next" button and follow the default procedure to install.



2. When you see the following dialog in setup procedure, it means setup is completed. If the "Launch the WarpSpeeder Tray Utility" checkbox is checked, the Tray Icon utility and [WarpSpeeder™] utility will be automatically and immediately launched after you click "Finish" button.



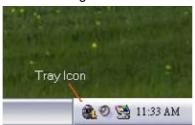
Usage:

The following figures are just only for reference, the screen printed in this user manual will change according to your motherboard on hand.

5.4 [WARPSPEEDERTM] INCLUDES 1 TRAY ICON AND 5 PANELS

1. Tray Icon:

Whenever the Tray Icon utility is launched, it will display a little tray icon on the right side of Windows Taskbar.



This utility is responsible for conveniently invoking [WarpSpeeder™] Utility. You can use the mouse by clicking the left button in order to invoke [WarpSpeeder™] directly from the little tray icon or you can right-click the little tray icon to pop up a popup menu as following figure. The "Launch Utility" item in the popup menu has the same function as mouse left-click on tray icon and "Exit" item will close Tray Icon utility if selected.

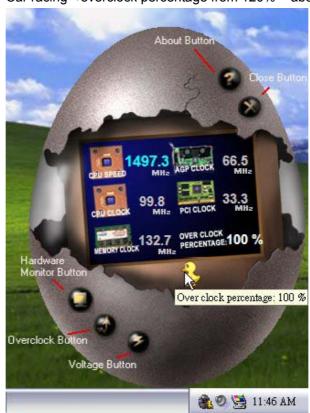


2. Main Panel

If you click the tray icon, [WarpSpeeder™] utility will be invoked. Please refer to the following figure; the utility's first window you will see is Main Panel.

Main Panel contains features as follows:

- a. Display the CPU Speed, CPU external clock, Memory clock, AGP clock, and PCI clock information.
- b. Contains About, Voltage, Overclock, and Hardware Monitor Buttons for invoking respective panels.
- c. With a user-friendly Status Animation, it can represent 3 overclock percentage stages:
 Man walking→overclock percentage from 100% ~ 110 %
 Panther running→overclock percentage from 110% ~ 120%
 Car racing→overclock percentage from 120% ~ above



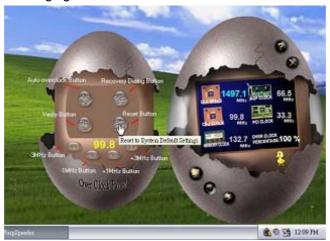
3. Voltage Panel

Click the Voltage button in Main Panel, the button will be highlighted and the Voltage Panel will slide out to up as the following figure. In this panel, you can decide to increase CPU core voltage and Memory voltage or not. The default setting is "No". If you want to get the best performance of overclocking, we recommend you click the option "Yes".



4. Overclock Panel

Click the Overclock button in Main Panel, the button will be highlighted and the Overclock Panel will slide out to left as the following figure.



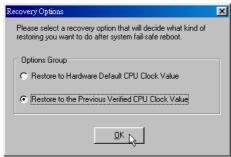
Overclock Panel contains the these features:

a. "–3MHz button", "-1MHz button", "+1MHz button", and "+3MHz button": provide user the ability to do real-time overclock adjustment.

Warning:

Manually overclock is potentially dangerous, especially when the overclocking percentage is over 110 %. We strongly recommend you verify every speed you overclock by click the Verify button. Or, you can just click Auto overclock button and let [WarpSpeeder™] automatically gets the best result for you.

b. "Recovery Dialog button": Pop up the following dialog. Let user select a restoring way if system need to do a fail-safe reboot.



P4M80-M7 & P4M80-M7A

- c. "Auto-overclock button": User can click this button and [WarpSpeeder™] will set the best and stable performance and frequency automatically. [WarpSpeeder™] utility will execute a series of testing until system fail. Then system will do fail-safe reboot by using Watchdog function. After reboot, the [WarpSpeeder™] utility will restore to the hardware default setting or load the verified best and stable frequency according to the Recovery Dialog's setting.
- d. "Verify button": User can click this button and [WarpSpeeder™] will proceed a testing for current frequency. If the testing is ok, then the current frequency will be saved into system registry. If the testing fail, system will do a fail-safe rebooting. After reboot, the [WarpSpeeder™] utility will restore to the hardware default setting or load the verified best and stable frequency according to the Recovery Dialog's setting.

Note

Because the testing programs, invoked in Auto-overclock and Verify, include DirectDraw, Direct3D and DirectShow tests, the DirectX 8.1 or newer runtime library is required. And please make sure your display card's color depth is High color (16 bit) or True color(24/32 bit) that is required for Direct3D rendering.

5. Hardware Monitor Panel

Click the Hardware Monitor button in Main Panel, the button will be highlighted and the Hardware Monitor panel will slide out to left as the following figure.

In this panel, you can get the real-time status information of your system. The information will be refreshed every 1 second.



6. About Panel

Click the "about" button in Main Panel, the button will be highlighted and the About Panel will slide out to up as the following figure.

In this panel, you can get model name and detail information in hints of all the chipset that are related to overclocking. You can also get the mainboard's BIOS model and the Version number of [WarpSpeeder $^{\text{TM}}$] utility.



P4M80-M7 & P4M80-M7A

Note:

Because the overclock, overvoltage, and hardware monitor features are controlled by several separate chipset, [WarpSpeeder™] divide these features to separate panels. If one chipset is not on board, the correlative button in Main panel will be disabled, but will not interfere other panels' functions. This property can make [WarpSpeeder™] utility more robust.

02/27, 2006

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BIOS Setup

Introduction

This manual discussed AwardTM Setup program built into the ROM BIOS. The Setup program allows users to modify the basic system configuration. This special information is then stored in battery-backed RAM so that it retains the Setup information when the power is turned off

The Award BIOSTM installed in your computer system's ROM (Read Only Memory) is a custom version of an industry standard BIOS. This means that it supports Intel Pentium [®] 4 processor input/output system. The BIOS provides critical low-level support for standard devices such as disk drives and serial and parallel ports.

Adding important has customized the Award BIOSTM, but nonstandard, features such as virus and password protection as well as special support for detailed fine-tuning of the chipset controlling the entire system.

The rest of this manual is intended to guide you through the process of configuring your system using Setup.

Plug and Play Support

These AWARD BIOS supports the Plug and Play Version 1.0A specification. ESCD (Extended System Configuration Data) write is supported.

EPA Green PC Support

This AWARD BIOS supports Version 1.03 of the EPA Green PC specification.

APM Support

These AWARD BIOS supports Version 1.1&1.2 of the Advanced Power Management (APM) specification. Power management features are implemented via the System Management Interrupt (SMI). Sleep and Suspend power management modes are supported. Power to the hard disk drives and video monitors can be managed by this AWARD BIOS.

ACPI Support

Award ACPI BIOS support Version 1.0 of Advanced Configuration and Power interface specification (ACPI). It provides ASL code for power management and device configuration capabilities as defined in the ACPI specification, developed by Microsoft, Intel and Toshiba.

PCI Bus Support

This AWARD BIOS also supports Version 2.1 of the Intel PCI (Peripheral Component Interconnect) local bus specification.

DRAM Support

DDR SDRAM (Double Data Rate Synchronous DRAM) are supported.

Supported CPUs

This AWARD BIOS supports the AMD CPU.

Using Setup

In general, you use the arrow keys to highlight items, press <Enter> to select, use the <PgUp> and <PgDn> keys to change entries, press <F1> for help and press <Esc> to quit. The following table provides more detail about how to navigate in the Setup program by using the keyboard.

Keystroke	Function	
Up arrow	Move to previous item	
Down arrow	Move to next item	
Left arrow	Move to the item on the left (menu bar)	
Right arrow	Move to the item on the right (menubar)	
Move Enter	Move to the item you desired	
PgUp key	Increase the numeric value or make changes	
PgDn key	Decrease the numeric value or make changes	
+ Key	Increase the numeric value or make changes	
- Key	Decrease the numeric value or make changes	
Esc key	Main Menu – Quit and not save changes into CMOS Status Page Setup Menu and Option Page Setup Menu – Exit Current page and return to Main Menu	
F1 key	General help on Setup navigation keys	
F5 key	Load previous values from CMOS	
F7 key	Load the optimized defaults	
F10 key	Save all the CMOS changes and exit	

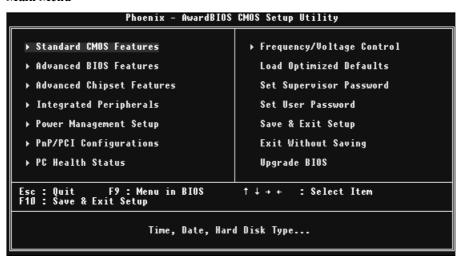
1 Main Menu

Once you enter Award BIOSTM CMOS Setup Utility, the Main Menu will appear on the screen. The Main Menu allows you to select from several setup functions. Use the arrow keys to select among the items and press <Enter> to accept and enter the sub-menu.

!! WARNING !!

The information about BIOS defaults on manual (Figure 1,2,3,4,5,6,7,8,9) is just for reference, please refer to the BIOS installed on board, for update information.

Main Menu



Standard CMOS Features

This submenu contains industry standard configurable options.

Advanced BIOS Features

This submenu allows you to configure enhanced features of the BIOS.

Advanced Chipset Features

This submenu allows you to configure special chipset features.

Integrated Peripherals

This submenu allows you to configure certain IDE hard drive options and Programmed Input/ Output features.

Power Management Setup

This submenu allows you to configure the power management features.

PnP/PCI Configurations

This submenu allows you to configure certain "Plug and Play" and PCI options.

PC Health Status

This submenu allows you to monitor the hardware of your system.

Frequency/Voltage Control

This submenu allows you to change CPU Vcore Voltage and CPU/PCI clock. (However, this function is strongly recommended not to use. Not properly change the voltage and clock may cause the CPU or M/B damage!)

Load Optimized Defaults

This selection allows you to reload the BIOS when the system is having problems particularly with the boot sequence. These configurations are factory settings optimized for this system. A confirmation message will be displayed before defaults are set.



Set Supervisor Password

Setting the supervisor password will prohibit everyone except the supervisor from making changes using the CMOS Setup Utility. You will be prompted with to enter a password.

Enter Password:

Set User Password

If the Supervisor Password is not set, then the User Password will function in the same way as the Supervisor Password. If the Supervisor Password is set and the User Password is set, the "User" will only be able to view configurations but will not be able to change them.



Save & Exit Setup

Save all configuration changes to CMOS(memory) and exit setup. Confirmation message will be displayed before proceeding.

SAUE to CMOS and EXIT (Y/N)? Y

Exit Without Saving

Abandon all changes made during the current session and exit setup. confirmation message will be displayed before proceeding.

Quit Without Saving (Y/N)? N

Upgrade BIOS

This submenu allows you to upgrade bios.

BIOS UPDATE UTILITY (Y/N)? Y

2 Standard CMOS Features

The items in Standard CMOS Setup Menu are divided into 10 categories. Each category includes no, one or more than one setup items. Use the arrow keys to highlight the item and than use the PgUID or PgDD keys to select the value you want in each item.

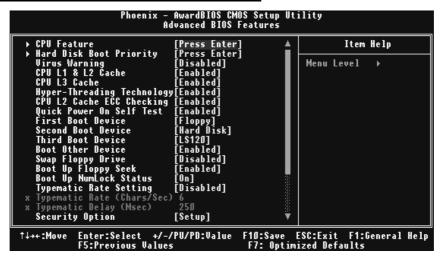
Main Menu Selections

This table shows the selections that you can make on the Main Menu.

Item	Options	Description
Date	mm: dd: yy	Set the system date. Note that the 'Day' automatically changes when you set the date.
Time	hh:mm:ss	Set the system internal clock.
IDE Primary Master	Options are in its sub menu.	Press <enter> to enter the sub menu of detailed options</enter>
IDE Primary Slave	Options are in its sub menu.	Press <enter> to enter the sub menu of detailed options.</enter>
IDE Secondary Master	Options are in its sub menu.	Press <enter> to enter the sub menu of detailed options.</enter>
IDE Secondary Slave	Options are in its sub menu.	Press <enter> to enter the sub menu of detailed options.</enter>
Drive A	360K, 5.25 in	Select the type of floppy
Drive B	1.2M, 5.25 in	disk drive installed in your
	720K, 3.5 in	system.
	1.44M, 3.5 in	
	2.88M, 3.5 in	
	None	
Video	EGA/VGA	Select the default video device.
	CGA 40	
	CGA 80	
	MONO	

ltem	Options	Description
Halt On	All Errors	Select the situation in which
	No Errors	you want the BIOS to stop
	All, but Keyboard	the POST process and
	All, but Diskette	notify you.
	All, but Disk/ Key	
Base Memory	N/A	Displays the amount of
		conventional memory
		detected during boot up.
Extended Memory	N/A	Displays the amount of
		extended memory detected
		during boot up.
Total Memory	N/A	Displays the total memory
		available in the system.

3 Advanced BIOS Features



3.1 CPU FEATURE



3.1.1 Delay Prior to Thermal

Set this item to enable the CPU Thermal function to engage after the specified time. The Choices: 4, 8, 16 (default), 32.

3.1.2 Thermal Management

Allow you to choose the thermal management method of your monitor. The Choices:Thermal Monitor 1 (default), Thermal Monitor 2. Notes:The choices will be different according to your CPU features.

3.1.3 TM2 Bus Ratio

Represents the frequency. Bus ratio of the throttled performance state that will be initiated when the on-die sensor goes from not hot to hot. **The Choices: 0X** (default).

3.1.4 TM2 Bus VID

Represents the voltage of the throttled performance state that will be initiated when the on-die sensor goes from not hot to hot.

The Choices:0.8375 (default).

3.1.5 Limit CPU ID Max Val

Set limit CPU ID maximum vale to 3, it should be disabled for Win XP. **The Choices: Disabled** (default), Enabled.

3.1.6 Execute Disable Bit

When disabled, forces the XD feature flag to always return 0. **The Choices: Disabled** (default), Enabled.

3.1.7 Virtualization Technology

This item Allow you to enabled or disabled Virtualization Technology. **The Choices: Enabled** (default), Disabled.

3.2 Hard Disk Boot Priority



These BIOS attempt to load the operating system from the device in the sequence selected in these items.

The Choices:

Pri. Master, Pri.Slave, Sec. Master, Sec. Slave, USBHDD0, USBHDD1, USBHDD2 and Bootable Add-in Carde.

3.3 Virus Warning

This option allows you to choose the Virus Warning feature that is used to protect the IDE Hard Disk boot sector. If this function is enabled and an attempt is made to write to the boot sector, BIOS will display a warning message on the screen and sound an alarm beep.

Disabled (default) Virus protection is disabled. Enabled Virus protection is activated.

3.4 CPU L1 & L2 Cache

Depending on the CPU/chipset in use, you may be able to increase memory access time with this option.

Enabled (default) Enable cache. Disabled Disable cache.

3.5 CPU L3 Cache

Depending on the CPU/chipset in use, you may be able to increase memory access time with this option.

Enabled (default) Enable cache.

Disabled Disable cache.

3.6 Hyper-Threading Technology

This option allows you to enable or disabled CPU Hyper-Threading. Enabled for Windows XP and Linux 2.4.x (OS optimized for Hyper Threading Technology. Disabled for other OS (OS not optimized for Hyper Threading Technology.

The Choices: Enabled (Default), Disabled.

3.7 CPU L2 Cache ECC Checking

This item allows you to enable/disable CPU L2 Cache ECC Checking.

The Choices: Enabled (default), Disabled.

3.8 Quick Power On Self Test

Enabling this option will cause an abridged version of the Power On Self-Test (POST) to execute after you power up the computer.

Enabled (default) Enable quick POST. Disabled Normal POST.

3.9 First/ Second/ Third/ Boot Other Device

These BIOS attempt to load the operating system from the device in the sequence selected in these items.

The Choices:Floppy, LS120, HDD-0, SCSI, CDROM, HDD-1, HDD-2, HDD-3, IP100, LAN, HPT370, Disabled, Enabled.

3.10 Swap Floppy Drive

For systems with two floppy drives, this option allows you to swap logical drive assignments.

The Choices:Disabled (default), Enabled.

3.11 Boot Up Floppy Seek

Enabling this option will test the floppy drives to determine if they have 40 or 80 tracks. Disabling this option reduces the time it takes to boot-up.

The Choices: Enabled (default), Disabled.

3.12 Boot Up NumLock Status

Selects the NumLock. State after power on.

The Choices: On (default) Numpad is number keys.

Off Numpad is arrow keys.

3.13 Typematic Rate Setting

When a key is held down, the keystroke will repeat at a rate determined by the keyboard controller. When enabled, the typematic rate and typematic delay can be configured.

The Choices:Disabled (default), Enabled.

3.14 Typematic Rate (Chars/Sec)

Sets the rate at which a keystroke is repeated when you hold the key down.

The Choices:6 (default), 8, 10, 12, 15, 20, 24, 30.

3.15 Typematic Delay (Msec)

Sets the delay time after the key is held down before it begins to repeat the keystroke.

The Choices:250 (default), 500,750,1000.

3.16 Security Option

This option will enable only individuals with passwords to bring the system online and/or to use the CMOS Setup Utility.

System A password is required for the system to boot and is also required to

access the Setup Utility.

Setup (default) A password is required to access the Setup Utility only.

This will only apply if passwords are set from the Setup main menu.

4 Advanced Chipset Features

This submenu allows you to configure the specific features of the chipset installed on your system. This chipset manage bus speeds and access to system memory resources, such as DRAM. It also coordinates communications with the PCI bus. The default settings that came with your system have been optimized and therefore should not be changed unless you are suspicious that the settings have been changed incorrectly.

4.1 DRAM Clock/Drive Control



To control the Clock/Drive. If you high light the literal "Press Enter" next to the "DRAM Clock/Drive Control" label and then press the enter key, it will take you a submenu with the following options:

4.1.1 DRAM Clock

This item determines DRAM clock following 100MHz, 133MHz, 166MHz or By SPD. **The Choices:** 100MHz, 133MHz, **By SPD** (default), 166MHz

4.1.2 DRAM Timing

This item determines DRAM clock/timing follow SPD or not. The Choices: Auto By SPD (default), Manual, Turbo, Ultra.

4.1.3 SDRAM CAS Latency

When SDRAM is installed, the number of clock cycles of CAS latency depends on the SDRAM timing.

The Choices: 2.5 /4 (default), 2.

4.1.4 Bank Interleave

This item allows you to enable or disable the bank interleave feature.

The Choices: Disabled (default), 2 bank, 4 bank.

4.1.5 Precharge to Active (Trp)

This items allows you to specify the delay from precharge command to activate command.

The Choices: 2T, 3T, 5T,4T (default).

4.1.6 Active to Precharge (Tras)

This items allows you to specify the minimum bank active time.

The Choices: 07T (default), 6T.

4.1.7 Active to CMD (Trcd)

Use this item to specify the delay from the activation of a bank to the time that a read or write command is accepted.

The Choices: 2T, 3T, 5T, 4T (default).

4.1.8 REF to ACT/REF to REF (Trfc)

This item allows you to determine the selection for REF to ACT/REF to REF (Trfc) $\,$

The Choices: 15T,25T (default).

4.1.9 ACT (0) to ACT(1) (TRRD)

This item allows you to determine the selection for ACT (0) to ACT(1) (TRRD).

The Choices: 3T (default).

4.1.10 Read to Precharge (Trtp)

This item allows you to determine the selection for Read to Precharge(Trtp).

The Choices: 2T (default).

4.1.11 Write to Read CMD (Twtr)

This item allows you to determine the selection for Write to Read CMD (Twtr).

The Choices: 1T/2T (default).

4.1.12 Write Recoverry Time (Twr)

This item allows you to determine the selection for Write Recoverry Time (Twr).

The Choices: 4T (default).

4.1.13 DRAM Command Rate

This item controls clock cycle that must occur between the last valid write operation and the next command.

The Choices: 1T Command, 2T Command (default).

4.1.14 RDSAIT mode

This item allows you to select the RDSAIT mode.

The Choices: Auto (default).

4.1.15 RDSAIT selection

This item allows you to make selection for RDSAIT.

The Choices: 03 (default).

```
4.2 AGP & P2P Bridge Control

Phoenix - AwardBIOS CMOS Setup Utility
AGP & P2P Bridge Control
                 Aperture Size
2.0 Mode
Driving Control
                                                                                                       Item Help
                                                     [4%]
[Auto]
                                                                                           Menu Level
                        Enter:Select +/-/PU/PD:Value
F5:Previous Values
                                                                        F10:Save ESC:Exit F1:General Help
F7: Optimized Defaults
```

If you highlight the literal "Press Enter" next to the "AGP & P2P Bridge Control" label and then press the enter key, it will take you a submenu with the following options:

4.2.1 **AGP Aperture Size**

Select the size of the Accelerated Graphics Port (AGP) aperture. The aperture is a portion of the PCI memory address range dedicated for graphics memory address space. Host cycles that hit the aperture range are forwarded to the AGP without any translation.

The Choices: 64M, 256M, 128M (default), 32M, 16M, 8M, 4M.

4.2.2 AGP 2.0 Mode

This item allows you to select the AGP Mode.

The Choices: 4X (default), 2X, 1X.

4.2.3 AGP Driving Control

By choosing "Auto" the system BIOS will the AGP output Buffer Drive strength P Ctrl by AGP Card. By choosing "Manual", it allows user to set AGP output Buffer Drive strength P Ctrl by manual.

The Choices: Auto (default), Manual.

4.2.4 AGP Driving Value

While AGP driving control item set to "Manual", it allows user to set AGP driving.

The Choices: DA (default).

4.2.5 AGP Fast Write

This item allows you to disabled or enabled AGP Fast Write.

The Choices: Disabled (default), Enabled.

4.2.6 AGP Master 1 WS Write

When Enabled, writes to the AGP (Accelerated Graphics Port) are executed with one-wait states.

The Choices: Disabled (default), Enabled.

4.2.7 AGP Master 1 WS Read

When Enabled, read to the AGP (Accelerated Graphics Port) are executed with one wait states.

The Choices: Disabled (default), Enabled.

4.2.8 AGP 3.0 Calibration cycle

This item allows you to disabled or enabled AGP 3.0 Calibration Cycle.

The Choices: Disabled (default), Enabled.

4.2.9 VGA Share Memory Size

This item allows you to select the VGA share memory size.

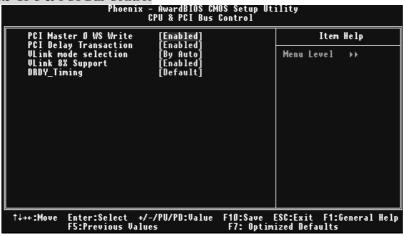
The Choices: 64M (default), Disabled.

4.2.10 Direct Frame Buffer

This item allows you to disabled or enabled direct frame buffer.

The Choices: Enabled (default), Disabled.

4.3 CPU & PCI Bus Control



If you highlight the literal "Press Enter" next to the "CPU & PCI Bus Control" label and then press the enter key, it will take you a submenu with the following options:

4.3.1 PCI Master 0 WS Write

When enabled, writes to the PCI bus are executed with zero-wait states. **The Choices: Enabled** (default), Disabled.

4.3.2 PCI Delay Transaction

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles.

Select Enabled to support compliance with PCI specification.

The Choices: Enabled (default), Disabled.

4.3.3 Vlink mode selection

This item allows you to select Vlink mode. **The Choices: By Auto** (default). Mode 1.

4.3.4 VLink 8X Support

This item allows you to enable or disable VLink 8X support.

The Choices: Enabled (default), Disabled.

4.3.5 DRDY_Timing

This item allows you to select DRDY_Timing.

The Choices: Default (default).

4.4 Memory Hole

When enabled, you can reserve an area of system memory for ISA adapter ROM. When this area is reserved, it cannot be cached. Refer to the user documentation of the peripheral you are installing for more information.

The Choices: Disabled (default), 15M – 16M.

4.5 System BIOS Cacheable

Selecting the "Enabled" option allows caching of the system BIOS ROM at F0000h-FFFFFh, which can improve system performance. However, any programs writing to this area of memory will cause conflicts and result in system errors.

The Choices: Enabled (default), Disabled

4.6 Init Display First

With systems that have multiple video cards, this option determines whether the primary display uses a PCI Slot or an AGP Slot.

The Choices: PCI Slot (default), AGP.

5 Integrated Peripherals

```
Phoenix - AwardBIOS CMOS Setup Utility
Integrated Peripherals

➤ VIA OnChip IDE Device [Press Enter]

➤ VIA OnChip PCI Device [Press Enter]

➤ SuperIO Device [Press Enter]

Menu Level →

↑↓++:Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help
F5:Previous Values F7: Optimized Defaults
```

5.1 VIA OnChip IDE Device



The chipset contains a PCI IDE interface with support for two IDE channels. Select "Enabled" to activate the first and / or second IDE interface. If you install a primary and / or secondary add-in IDE interface, select "Disabled" to deactivate an interface. If you high light the literal "Press Enter" next to the "Onchip IDE Control" label and then press the enter key, it will take you a submenu with the following options:

5.1.1 OnChip SATA

This option allows you to enable the onchip Serial ATA.

The Choices: Enabled (default), Disabled.

5.1.2 SATA Mode

This option allows you to select SATA Mode.

The Choices: RAID (default), IDE.

5.1.3 IDE DMA Transfer Access

This item allows you to enable or disable the IDE transfer access.

The Choices: Enabled (default), Disabled.

5.1.3 **IDE** Channel 0/1

The motherboard chipset contains a PCI IDE interface with support for two IDE channels. Select "Enabled" to activate the first and/or second IDE interface. Select "Disabled" to deactivate an interface if you are going to install a primary and/or secondary add-in IDE interface.

The Choices: Enabled (default), OnChip, Disabled.

5.1.4 IDE Prefetch Mode

The "onboard" IDE drive interfaces supports IDE prefetching for faster drive access. If the interface does not support prefetching. If you install a primary and/or secondary add-in IDE interface, set this option to "Disabled".

The Choices: Enabled (default), Disabled.

5.1.5 IDE Primary/Secondary Master/Slave PIO

The IDE PIO (Programmed Input / Output) fields let you set a PIO mode (0-4) for each of the IDE devices that the onboard IDE interface supports. Modes 0 through 4 provides successively increased performance. In Auto mode, the system automatically determines the best mode for each device.

The Choices: Auto (default), Mode0, Mode1, Mode2, Mode3, Mode4.

5.1.6 IDE Primary/Secondary Master/Slave UDMA

Ultra DMA/100 functionality can be implemented if it is supported by the IDE hard drives in your system. As well, your operating environment requires a DMA driver (Windows 95 OSR2 or a third party IDE bus master driver). If your hard drive and your system software both support Ultra DMA/100, select Auto to enable BIOS support.

The Choices: Auto (default), Disabled.

5.1.7 IDE HDD Block Mode

If your IDE hard drive supports block mode, select "Enabled" for automatic detection of the optimal number of block read/ writes per sector the drive can support.

The Choices: Enabled (default), Disabled.

5.2 VIA OnChip PCI Device



If you highlight the literal "Press Enter" next to the "OnChip PCI Device" label and then press the enter key, it will take you a submenu with the following options:

5.2.1 VIA-3058 AC97 Audio

This option allows you to control the onboard AC97 audio.

The Choices: Auto (default), Disabled.

5.2.2 VIA-3068 MC97 Modem

This option allows you to control the onboard MC97 modem.

The Choices: Auto (default), Disabled.

5.2.3 VIA-3043 OnChip LAN(Enabled)

This option allows you to control the onboard VIA-3043 OnChip LAN.

The Choices: Enabled (default), Disabled.

5.2.4 Onboard LAN Boot ROM

This item allows you to enable or disable the Onboard LAN Boot ROM controller.

The Choices: Disabled (default), Enabled.

5.2.5 Onchip USB Controller

Select "Enabled" if your system contains a Universal Serial Bus (USB) controller and you have USB peripherals.

The Choices: All Enabled (default).

5.2.6 On-chip EHCI Controller

This item allows you to enable or disable the on-chip EHCI controller.

The Choices: Enabled (Default), Disabled.

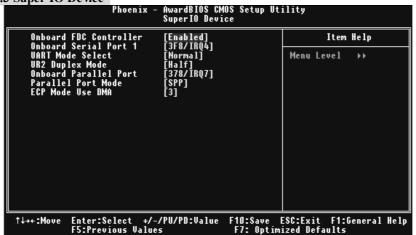
5.2.7 USB Emulation

Do not support any USB device on Dos **The Choices:** Off, **On** (default).

5.2.8 USB Keyboard/ Mouse Support

This item allows you to enable or disable the USB Keyboard/ Mouse Legacy Support. **The Choices:Enabled** (default), Disabled.

5.3 Super IO Device



If you highlight the literal "Press Enter" next to the "Super IO Device" label and then press the enter key, it will take you a submenu with the following options:

5.3.1 Onboard FDC Controller

Select Enabled if your system has a floppy disk controller (FDC) installed on the system board and you wish to use it. If install and FDC or the system has no floppy drive, select Disabled in this field.

The Choices: Enabled (default), Disabled.

5.3.2 Onboard Serial Port 1

Select an address and corresponding interrupt for the first and second serial ports. **The Choices:**Disabled, **3F8/IRQ4** (default), 2F8/IRQ3, 3E8/IRQ4, 2E8/IRQ3, Auto.

5.3.3 UART Mode Select

This item allows you to determine which Infra Red (IR) function of onboard I/O chip. **The Choices:Normal** (default), AS KIR, IrDA.

5.3.4 UR2 Duplex Mode

elect the value required by the IR device connected to the IR port. Full-duplex mode permits simultaneous two-direction transmission. Half-duplex mode permits transmission in one direction only at a time.

The Choices: Half (default), Full.

5.3.5 Onboard Parallel Port

This item allows you to determine access onboard parallel port controller with which I/O Address.

The Choices:378/IRQ7 (default), 278/IRQ5, 3BC/IRQ7, Disabled.

5.3.6 Parallel Port Mode

The default value is SPP.

The Choices:

SPP(default) Using Parallel port as Standard Printer Port.

EPP Using Parallel Port as Enhanced Parallel Port.

ECP Using Parallel port as Extended Capabilities Port.

ECP+EPP Using Parallel port as ECP & EPP mode.

5.3.7 ECP Mode Use DMA

Select a DMA Channel for the port.

The Choices: 3 (default), 1.

6 Power Management Setup

The Power Management Setup Menu allows you to configure your system to utilize energy

6.1 ACPI function

This item displays the status of the Advanced Configuration and Power Management (ACPI).

The Choices: Enabled (default), Disabled.

6.2 Power Management Option

This category allows you to select the type (or degree) of power saving and is directly related to the following modes:

- 1.HDD Power Down.
- 2. Suspend Mode.

There are four options of Power Management, three of which have fixed mode settings Min. Power Saving

> Minimum power management. Suspend Mode = 1 hr. HDD Power Down = 15 min Max. Power Saving

Maximum power management only available for sl CPU's. Suspend Mode = 1 min. HDD Power Down = 1 min.

User Define (default)

Allows you to set each mode individually.

When not disabled, each of the ranges are from 1 min. to 1 hr. except for HDD Power Down which ranges from 1 min. to 15 min. and disable.

6.3 HDD Power Down

When enabled, the hard disk drive will power down and after a set time of system inactivity. All other devices remain active.

The Choices: Disabled (default), 1 Min, 2 Min, 3 Min, 4 Min, 5 Min, 6 Min, 7 Min, 8 Min, 9 Min, 10 Min, 11 Min, 12 Min, 13 Min, 14 Min, 15 Min.

6.4 Suspend Mode

The item allows you to select the suspend type under ACPI operating system.

The Choices: Disabled (default), 1 Min, 2 Min, 4 Min, 6 Min, 8 Min, 10 Min, 20 Min, 30 Min, 40 Min, 1 Hour.

6.5 Video Off Option

This field determines when to activate the video off feature for monitor power management.

The Choices: Suspend→Off (default), Always on.

6.6 Video Off Method

This option determines the manner in which the monitor is goes blank.

V/H SYNC+Blank (default)

This selection will cause the system to turn off the vertical and horizontal synchronization ports and write blanks to the video buffer.

Blank Screen

This option only writes blanks to the video buffer.

DPMS

Initial display power management signaling. The Choices: Stop Grant, PwrOn Suspend.

6.7 Modem Use IRQ

This determines the IRQ, which can be applied in MODEM use.

The Choices: 3 (default),4 / 5 / 7 / 9 / 10 / 11 / NA.

6.8 Soft-Off by PWR-BTN

Pressing the power button for more than 4 seconds forces the system to enter the Soft-Off state when the system has "hung."

The Choices: Delay 4 Sec, Instant-Off (default).

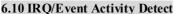
6.9 Ac Loss Auto Restart

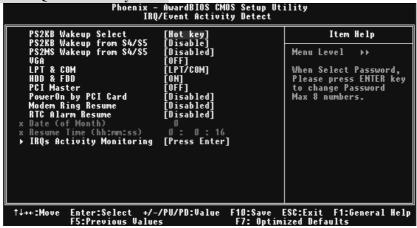
This field determines the action the system will automatically take when power is restored to a system that had lost power previously without any subsequent manual intervention. There are 3 sources that provide current to the CMOS area that retains these Power-On instructions; the motherboard battery (3V), the Power Supply (5VSB), and the Power Supply (3.3V). While AC is not supplying power, the motherboard uses the motherboard battery (3V). If AC power is supplied and the Power Supply is not turned on, 5VSB from the Power Supply is used. When the Power Supply is eventually turned on 3.3V from the Power Supply will be used.

There are 3 options: "Former-Sts", "On", "Off".

"Off" (default) Means always set CMOS to the "Off" status when AC power is lost. "On" Means always set CMOS to the "On" status when AC power is lost

"Former-Sts" Means to maintain the last status of the CMOS when AC power is lost. For example: If set to "Former-Sts" and AC power is lost when system is live, then after AC power is restored, the system will automatically power on. If AC power is lost when system is not live, system will remain powered off.





If you highlight the literal "Press Enter" next to the "IRQ/Event Activity Detect" label and then press the enter key, it will take you a submenu with the following options:

6.12.1 PS2KB Wakeup Select

When select Password, please press Enter key to change password with a maximum of 8 characters.

The Choices: Hot Key (default).

6.12.2 PS2KB Wakeup from S3/S4/S5

This item allows you to wake up from S3/ S4/ S5 with PS2 keyboard. **The Choices: Disabled** (default), Ctrl+F1, Ctrl+F2. Ctrl+F3, Ctrl+F4, Ctrl+F5, Ctrl+F6, Ctrl+F7, Ctrl+F8, Ctrl+F9, Ctrl+F10, Ctrl+F11, Ctrl+F12, Power, Wake, Any Key.

6.12.3 PS2MS Wakeup from S3/ S4/ S5

This item allows you to wake up from S3/ S4/ S5 with PS2 keyboard. **The Choices: Disabled** (default), Ctrl+F1, Ctrl+F2. Ctrl+F3, Ctrl+F4, Ctrl+F5, Ctrl+F6, Ctrl+F7, Ctrl+F8, Ctrl+F9, Ctrl+F10, Ctrl+F11, Ctrl+F12, Power, Wake, Any Key.

6.12.4 VGA

When set to **On**, any event occurring at a VGA Port will awaken a system which has been powered down.

The Choices: Off (default), On.

6.12.5 LPT & COM

When this option is set to **On**, any event occurring at a COM(serial)/LPT (printer) port will awaken a system which has been powered down.

The Choices: LPT/COM (default), COM, LPT, NONE.

6.12.6 HDD & FDD

When this option is set to **On**, any event occurring on a hard drive or a floppy drive will awaken a system which has been powered down.

The Choices: On (default), Off.

6.12.7 PCI Master

When set to \mathbf{On} , you need a LAN add-on card which supports the power function. It should also support the wake-up on LAN jump.

The Choices: Off (default), On.

6.12.8 Power On by PCI Card

When you select Enabled, a PME signal from PCI card returns the system to Full ON state

The Choices: Disabled (default), Enabled.

6.12.9 Modem Ring Resume

This item allows you to disable or enable Modem Ring Resume.

The Choices: Disabled (default), Enabled.

6.12.10 RTC Alarm Resume

When "Enabled", you can set the date and time at which the RTC (real-time clock) alarm awakens the system from Suspend mode.

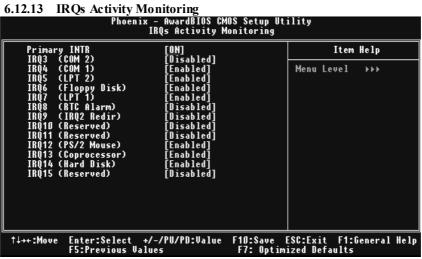
The Choices: Enabled, Disabled (default).

6.12.11 Date (of Month)

You can choose which month the system will boot up. This field is only configurable when "RTC Resume" is set to "Enabled"

6.12.12 Resume Time (hh:mm:ss)

You can choose the hour, minute and second the system will boot up. This field is only configurable when "RTC Resume" is set to "Enabled".

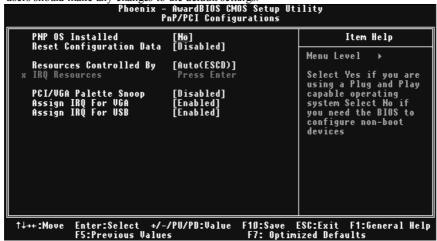


Press Enter to access another sub menu used to configure the different wake up events (i.e. wake on LPT & COMM activity).

Primary INTR	On
IRQ3 (COM2)	Enabled
IRQ4 (COM1)	Enabled
IRQ5 (LPT2)	Enabled
IRQ6 (Floppy Disk)	Enabled
IRQ7 (LPT1)	Enabled
IRQ8 (RTC Alarm)	Disabled
IRQ9 (IRQ2 Redir)	Disabled
IRQ10 (Reserved)	Disabled
IRQ11 (Reserved)	Disabled
IRQ12 (PS/2 Mouse)	Enabled
IRQ13 (Coprocessor)	Enabled
IRQ14 (Hard Disk)	Enabled
IRQ15 (Reserved)	Disabled

7 PnP/PCI Configurations

This section describes configuring the PCI bus system. PCI, or Personal Computer Interconnect, is a system which allows I/O devices to operate at speeds nearing the speed of the CPU itself uses when communicating with its own special components. This section covers some very technical items and it is strongly recommended that only experienced users should make any changes to the default settings.



7.1 PNP OS Installed

When set to YES, BIOS will only initialize the PnP cards used for the boot sequence (VGA, IDE, SCSI). The rest of the cards will be initialized by the PnP operating system like WindowTM 95. When set to NO, BIOS will initialize all the PnP cards. For non-PnP operating systems (DOS, NetwareTM), this option must set to NO.

The Choices: No (default), Yes.

7.2 Reset Configuration Data

The system BIOS supports the PnP feature which requires the system to record which resources are assigned and protects resources from conflict. Every peripheral device has a node, which is called ESCD. This node records which resources are assigned to it. The system needs to record and update ESCD to the memory locations. These locations (4K) are reserved in the system BIOS. If the Disabled (default) option is chosen, the system's ESCD will update only when the new configuration varies from the last one. If the Enabled option is chosen, the system is forced to update ESCDs and then is automatically set to the "Disabled" mode.

The above settings will be shown on the screen only if "Manual" is chosen for the resources controlled by function.

Legacy is the term, which signifies that a resource is assigned to the ISA Bus and provides non-PnP ISA add-on cards. PCI / ISA PnP signifies that a resource is assigned to the PCI Bus or provides for ISA PnP add-on cards and peripherals.

The Choices: Disabled (default), Enabled.

7.3 Resources Controlled By

By Choosing "Auto(ESCD)" (default), the system BIOS will detect the system resources and automatically assign the relative IRQ and DMA channel for each peripheral.By Choosing "Manual", the user will need to assign IRQ & DMA for add-on cards. Be sure that there are no IRQ/DMA and I/O port conflicts.

7.4 IRQ Resources

This submenu will allow you to assign each system interrupt a type, depending on the type of device using the interrupt. When you press the "Press Enter" tag, you will be directed to a submenu that will allow you to configure the system interrupts. This is only configurable when "Resources Controlled By" is set to "Manual".

IRQ-3	assigned to	PCI Device
IRQ-4	assigned to	PCI Device
IRQ-5	assigned to	PCI Device
IRQ-7	assigned to	PCI Device
IRQ-9	assigned to	PCI Device
IRQ-10	assigned to	PCI Device
IRQ-11	assigned to	PCI Device
IRQ-12	assigned to	PCI Device
IRQ-14	assigned to	PCI Device
IRQ-15	assigned to	PCI Device

7.5 PCI / VGA Palette Snoop

Choose Disabled or Enabled. Some graphic controllers which are not VGA compatible take the output from a VGA controller and map it to their display as a way to provide boot information and VGA compatibility.

However, the color information coming from the VGA controller is drawn from the palette table inside the VGA controller to generate the proper colors, and the graphic controller needs to know what is in the palette of the VGA controller. To do this, the non-VGA graphic controller watches for the Write access to the VGA palette and registers the snoop data. In PCI based systems, where the VGA controller is on the PCI bus and a non-VGA graphic controller is on an ISA bus, the Write Access to the palette will not show up on the ISA bus if the PCI VGA controller responds to the Write

In this case, the PCI VGA controller should not respond to the Write, it should only snoop the data and permit the access to be forwarded to the ISA bus. The non-VGA ISA graphic controller can then snoop the data on the ISA bus. Unless you have the above situation, you should disable this option.

Disabled (default) Disables the function. Enabled Enables the function.

7.6 Assign IRQ For VGA

This item allows the users to choose which IRQ to assign for the VGA. **The Choices: Enabled** (default), Disabled.

7.7 Assign IRQ For USB

This item allows the users to choose which IRQ to assign for the USB. **The Choices: Enabled** (default), Disabled.

8 PC Health Status



8.1 Shutdown Temperature

This item allows you to set up the CPU shutdown Temperature. This item only effective under Windows 98 ACPI mode.

The Choices: Disabled (default), Enabled.

8.2 CPU FAN Control by

The Choice "smart" can make your CPU FAN to reduce noise.

The Choices: SMART (default), Always On.

8.3 CPU Vcore +3.3 V, +5.0 V, DRAM Voltage, AGP Voltage, Voltage Battery

Detect the system's voltage status automatically.

8.4 Current CPU Temp

This field displays the current temperature of CPU.

8.5 Current CPU FAN Speed

This field displays the current speed of CPU fan.

8.6 Current SYS FAN Speed

This field displays the current speed SYSTEM fan.

8.7 Show H/W Monitor in POST

If you computer contain a monitoring system, it will show PC health status during POST stage. The item offers several delay time to select you want.

The Choices: Enabled (default), Disabled .

8.8 Chassis Open Warning

This item allows you to enable or disable Chassis Open Warning beep. **The Choices:** 60oC/140oF, 65oC/149oF, 70oC/158oF, **Disabled** (default).

9 Frequency/ Voltage Control



9.1 CPU Clock Ratio

The Choices: 8X (default), 9X, 10X, 11X, 12X, 13X, 14 X, 15X, 16X, 17X, 18X, 19X, 20 X, 21 X, 22 X, and 23X.

9.2 CPU Voltage Regulator

This item allows you to select CPU Voltage Control.

The Choices: Default (default)

9.3 DDR Voltage Regulator

This item allows you to select DDR Voltage Control.

The Choices: Default, 2.65V (default)

9.4 AutoDetect PCI/DIMM Clk

This item allows you to enable / disable auto Detect PCI Clock.

The Choices: Enabled (default), Disabled.

9.5 Spread Spectrum(Enabled)

This item allows you to enable/disable the Spread Spectrum function.

The Choices: Enabled (default), Disabled

9.6 CPU CLOCK

This item allows you to select CPU Clock, and CPU over clocking. **The Choices: 100MHZ** (default).

Special Notice:

If unfortunately, the system's frequency that you are selected is not functioning, there are two methods of booting-up the system.

Method 1:

Clear the COMS data by setting the JCOMS1 ((2-3) closed)) as "ON" status. All the CMOS data will be loaded as defaults setting.

Method 2:

Press the <Insert> key and Power button simultaneously, after that keep-on pressing the <Insert> key until the power-on screen showed. This action will boot-up the system according to FSB of the processor

It's strongly recommended to set CPU V core and clock in default setting. If the CPU V core and clock are not in default setting, it may cause CPU or M/B damage.